

Ch. 10 Thin Film Process

陳國聲

1

Outline

- Thin film applications
- Overview of deposition
- Physical vapor deposition
- Chemical vapor deposition
- Epitaxial growth
- Thin film materials sciences

2

Part I:Thin film applications

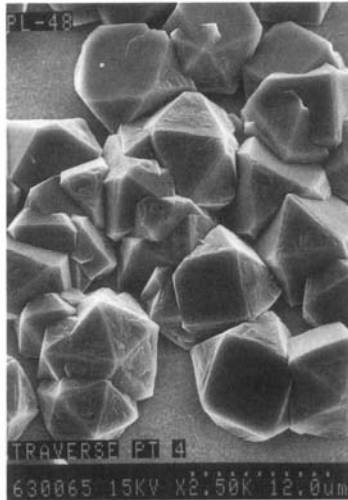
3

Why Thin Films?

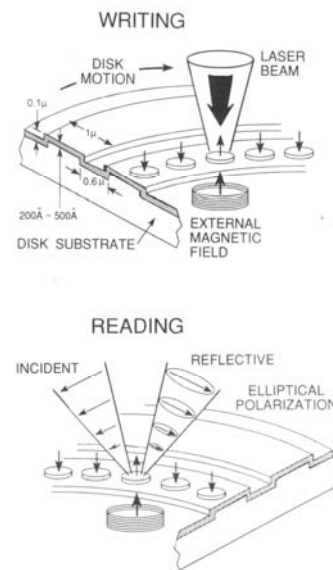
- Thin films are required for many steps in semiconductor fabrication
 - Masks
 - Isolated elements
 - Metal interconnections
 - MOS gate
 - Barriers

4

Examples



CVD Diamond



MO

5

Applications: Interconnection

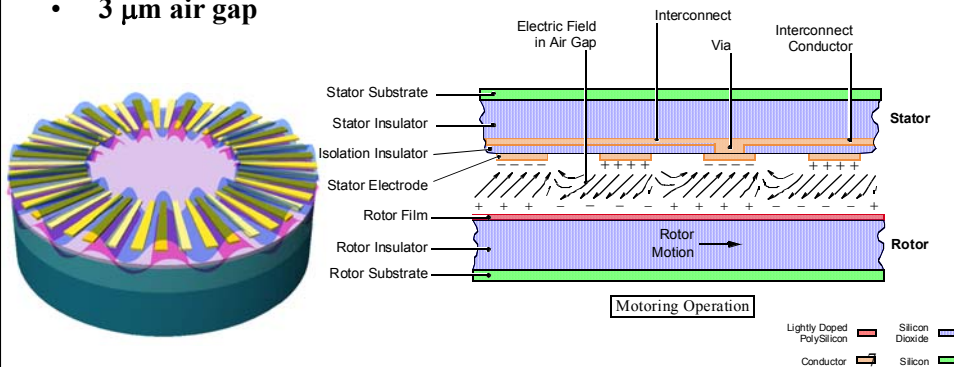


6

Electric Induction Micro-Motor

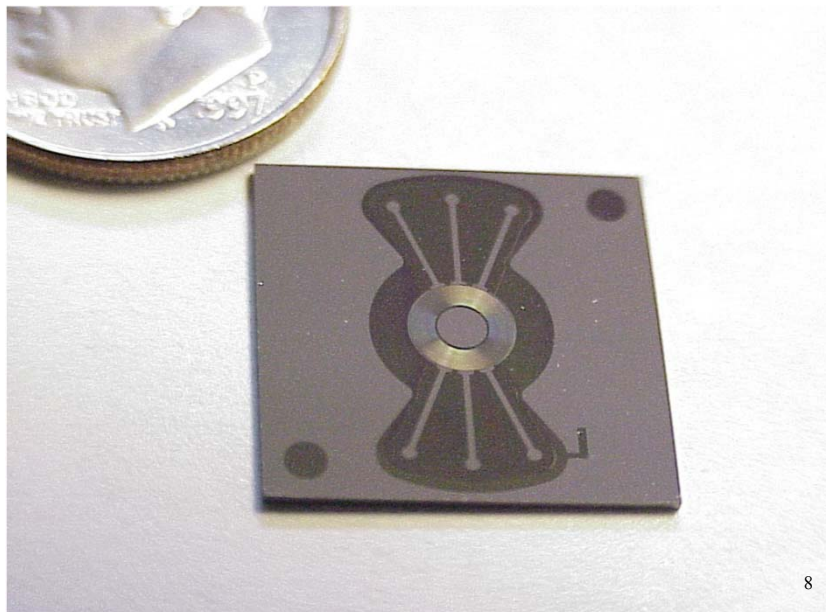
Torque applied across air gap by inducing and dragging charges in the rotor film:

- Polar array of 786 electrodes connected in 6 phase
- Sinusoidal stator excitation of $\pm 300\text{V}$ at 3 MHz
- Rotor coated with a film of controlled conductivity
- 3 μm air gap

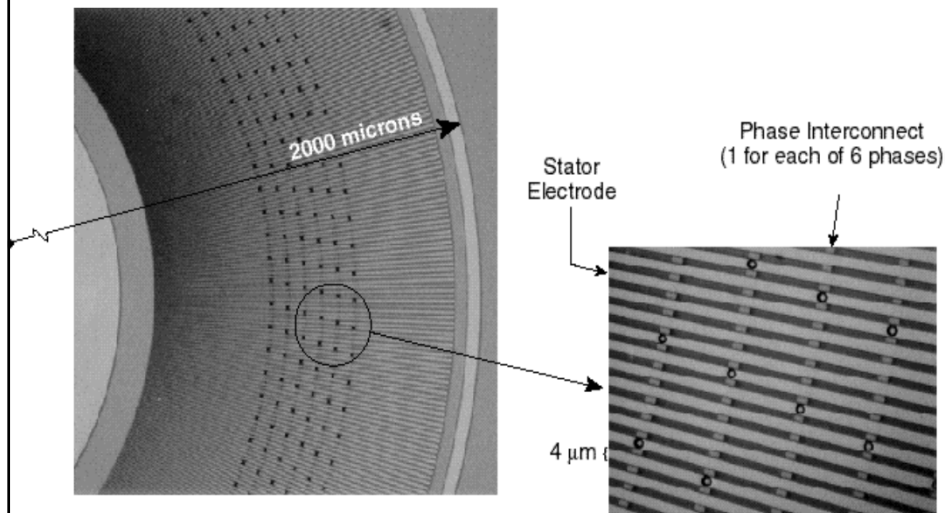


In Collaboration with S. F. Nagle & J. H. Lang, MIT

Two-Level Polysilicon Stator Device



Two-Level Polysilicon Stator Device

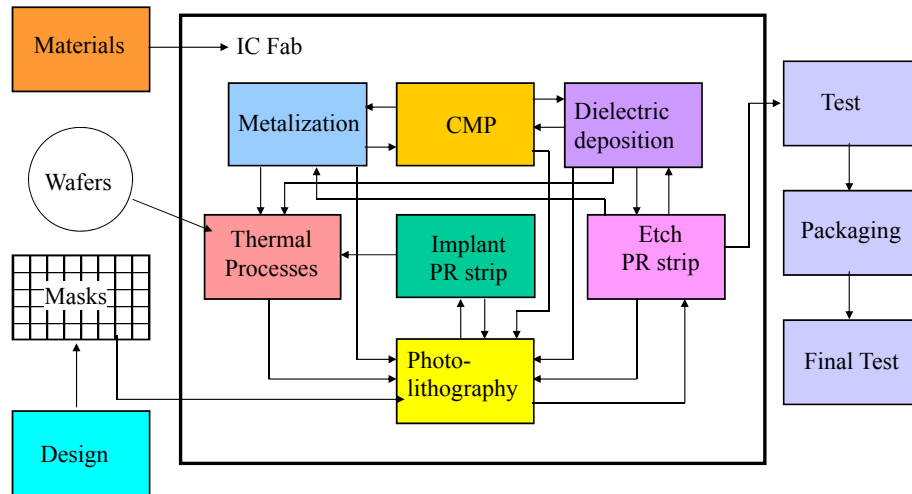


9

Part II: Overview of Deposition

10

Wafer Process Flow



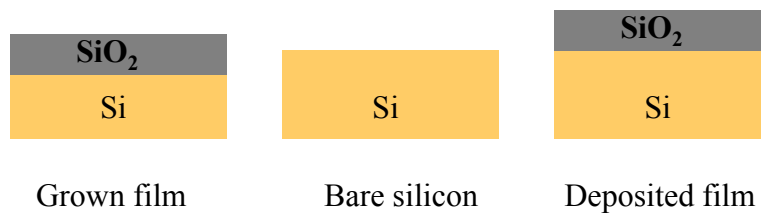
11

Applications of Deposited Films

- Gate materials
 - polysilicon
- Conductive layers
 - Metal lines
- Dielectric layers
 - Inter-metallic dielectric (IMD)

12

Example: CVD Oxide vs. Grown Oxide



13

Example: CVD Oxide vs. Grown Oxide

Grow

- Oxygen is from gas phase
- Silicon from substrate
- Oxide grow into silicon
- Higher quality

CVD

- Both oxygen and silicon are from gas phase
- Deposit on substrate surface
- Lower temperature
- Higher growth rate

14

Dielectric Thin Film Applications

- Multi-level metal interconnection
- CVD and SOG plus CVD dielectrics
- Shallow trench isolation (STI)
- Sidewall spacer for salicide, LDD, and the source/drain diffusion buffer
- The passivation dielectric (PD)
- Dielectric ARC for feature size $< 0.25 \mu\text{m}$

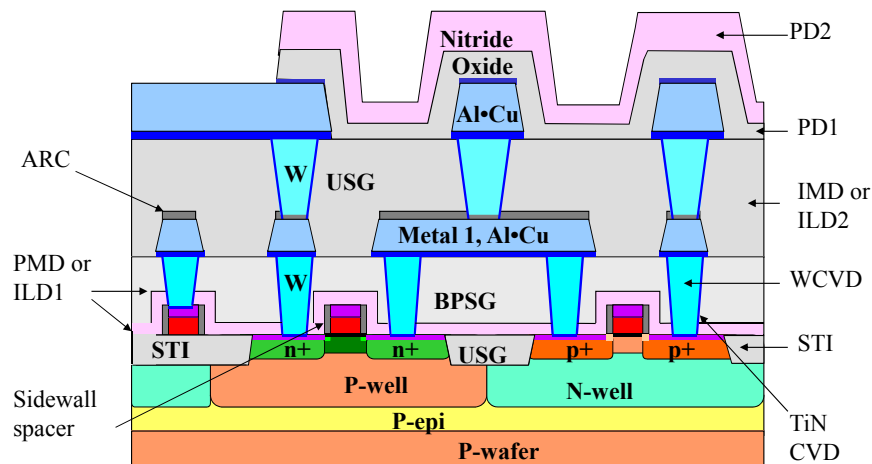
15

Dielectric Thin Film Applications

- Inter layer dielectric, or ILD, include PMD and IMD
- Pre-metal dielectric: PMD
 - normally PSG or BPSG
 - Temperature limited by thermal budget
- Inter-metal dielectric: IMD
 - USG or FSG
 - Normally deposited around 400°C

16

Figure 10.2



17

Key Performance Index of Thin Film Processes

- Deposition rate
- Dimension control
- Residual stress
- Conformality
 - Step coverage
 - Flatness
 - Surface roughness
- Adhesion of film to substrate

18

Key Considerations of Evaporation

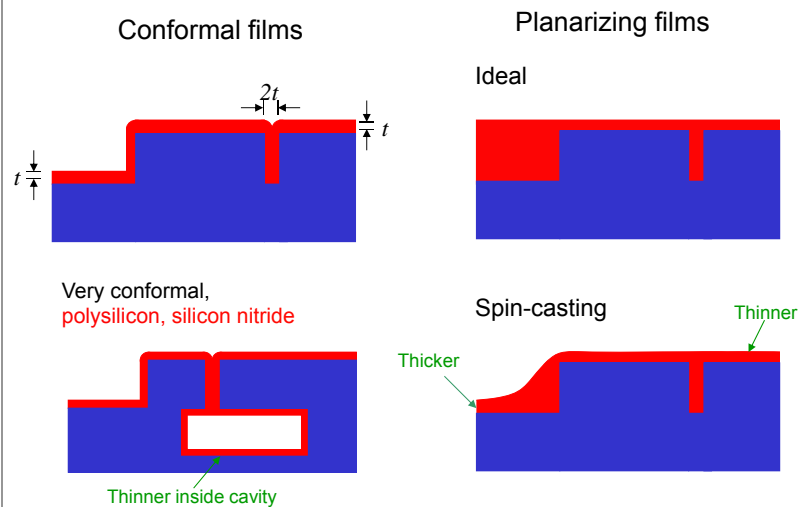
- Deposition rate
- Temperature
 - some metals requires excessive high temperature
- Contamination
- Step Coverage
 - an important issue in evaluating performance of deposition, usually poor in evaporation
- Residual stresses
 - usually due to mismatch of CTE and micro structures transformation

19

STATE UNIVERSITY OF NEW YORK at STONY BROOK

Yu-Hsuan Su

Conformality (Step Coverage)



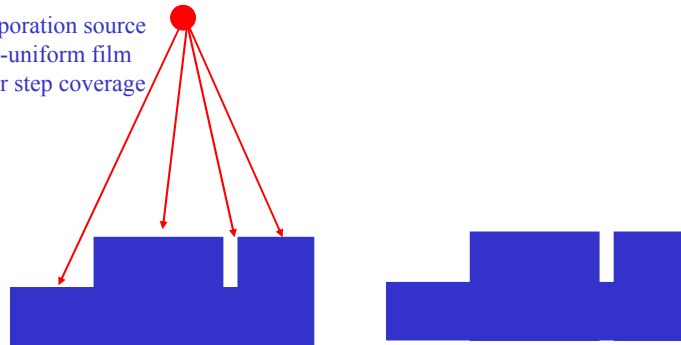
20

Conformality (Step Coverage)

Non-conformal films

Line of sight

evaporation source
non-uniform film
poor step coverage



21

Part III: Physical Vapor Deposition

22

PVD

- Deposit film by physical methods
 - Thermal evaporation
 - Material source is heated to sublimation temperature in a vacuum
 - Material is vapor transported to target in vacuum
 - Deposition is by "line-of-sight" (mean free path = 50m)
 - Sputtering
 - Material is removed from target by momentum transfer
 - Gas molecules are ionized in a glow discharge, ions strike target and remove mainly neutral atoms
 - Atoms condense on the substrate
 - Vacuum level ~ 10 mtorr --> mean free path ~ 5 mm
 - Easy to deposit alloys

23

蒸鍍原理

1. 控制蒸氣壓以調節原子到達的數量

$$\log P \text{ (torr)} = A - (\Delta H_v / 2.3RT)$$

2. 原子到達的能量

$$\text{Thermal Energy} = 3kT/2$$

e.g. 0.1 eV at 500°C
 0.2 eV at 1265°C

24

真空的要求

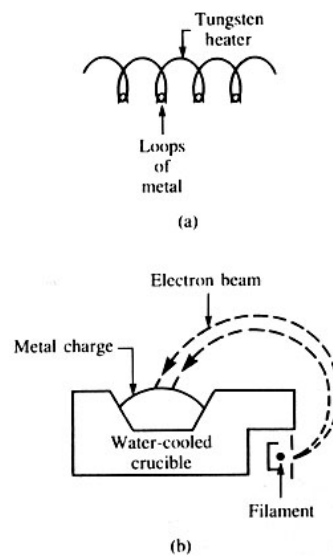
- ❑ 真空中殘留氣體分子有機會在薄膜成長過程中摻入，基板溫度愈高則機會愈小。
- ❑ 真空中原子碰撞的平均自由路徑(Mean free path)，e.g. $L = 5 \text{ cm}$ when $P = 10^{-3} \text{ torr}$; $L = 5000 \text{ cm}$ when $P = 10^{-6} \text{ torr}$

$$L(\text{cm}) = \frac{5 \times 10^{-3}}{P(\text{torr})}$$

25

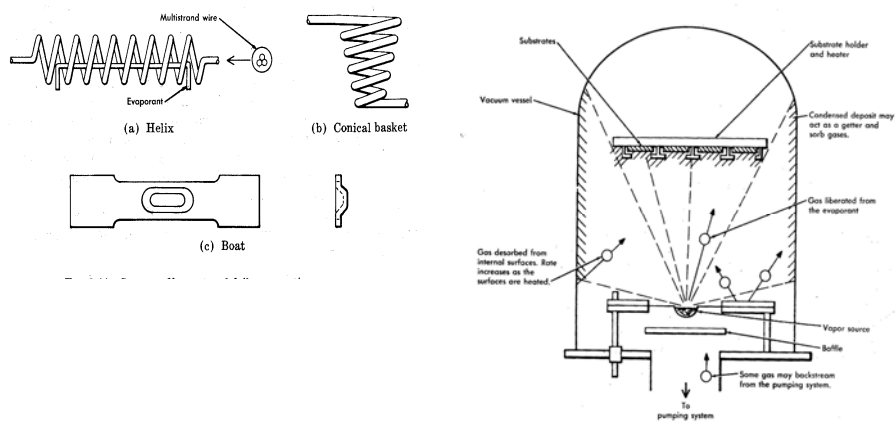
Type of Evaporation

- Filament evaporation
 - major problems
 - high contamination level
 - hard to form composite films
- Electron-beam evaporation
 - using high density electron beam to evaporate metals
 - dual E-beams with dual target can be used to coevaporate composite materials
 - major problem: radiation damage



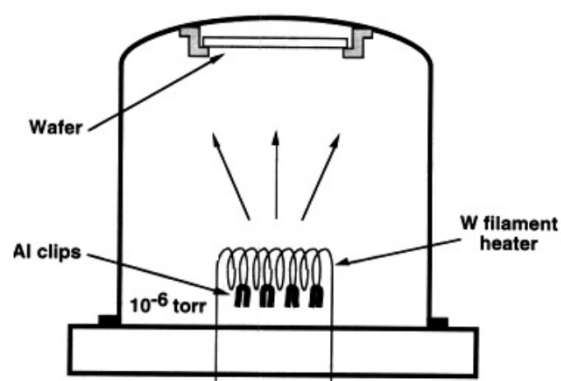
26

熱蒸鍍 (Thermal Evaporation)



27

Evaporation THERMAL EVAPORATION

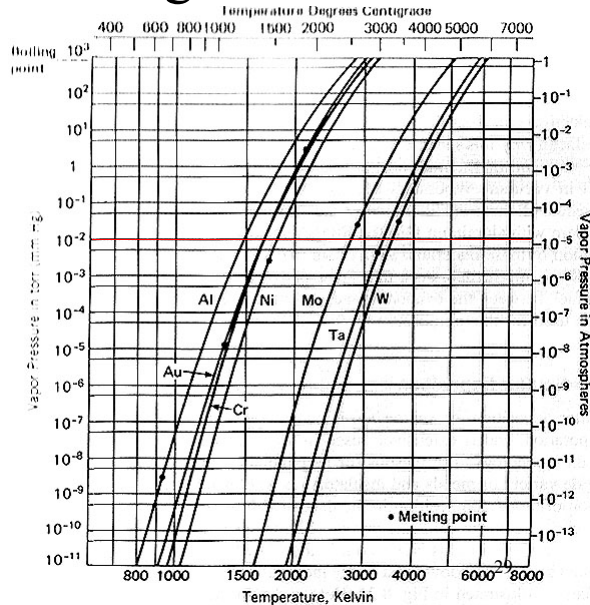


- Need high temperature
- Need high vacuum environment

28

Phase Diagram

- Usually at least 10 mTorr is required to obtain reasonable deposition rate
- Refractory metals required very high temperature
 - W, Ta, Mo,



Applications

- Usually widely used in GaAs technology
 - in silicon, it was replaced by sputtering in most fields
- A necessary step for many other applications
 - SEM
 - to form a thin conduction layer on specimen surface
 - LIGA or LIGA-like MEMS processes
 - need to form a conduction layer on PMMA for the subsequent electroplating process
- Since high temperature is required, usually this technology is restricted on metal only

Key Considerations of Evaporation

- Deposition rate
- Temperature
 - some metals requires excessive high temperature
- Contamination
- Step Coverage
 - an important issue in evaluating performance of deposition, usually poor in evaporation
- Residual stresses
 - usually due to mismatch of CTE and micro structures transformation

31

Step Coverage

- A primary limitation of evaporation
 - material beams are nondivergent
- Need wafer rotation to improve step coverage
- Performance index
 - AR (step height/step diameter)
 - OK for $AR < 0.5$
 - marginal $0.5 < AR < 1$
 - poor if $AR > 1$

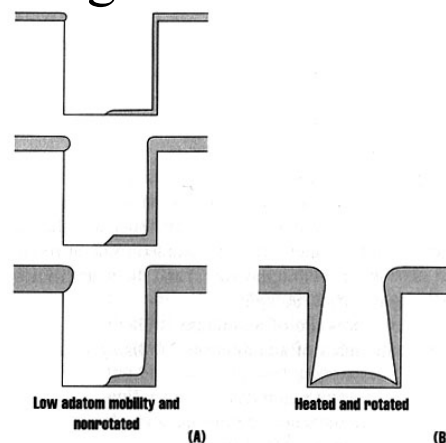
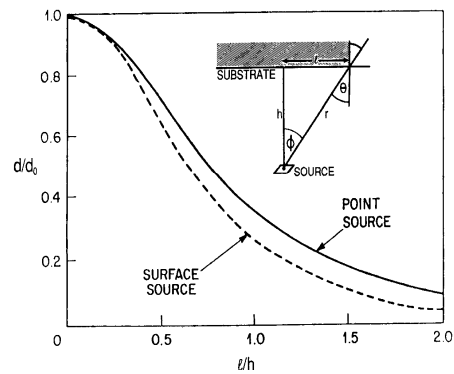
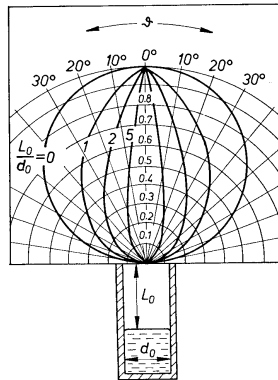


Figure 12-5 (a) Time evolution of the evaporative coating of a feature with aspect ratio of 1.0, with little surface atom mobility (i.e., low substrate temperature) and no rotation. (b) Final profile of deposition on rotated and heated substrates.

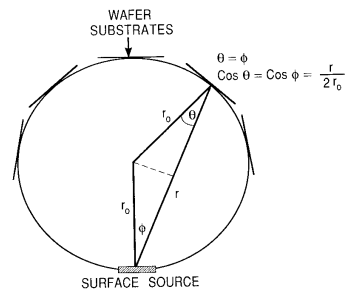
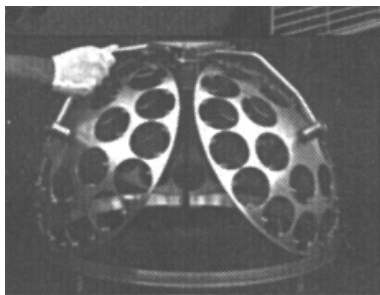
32

Film Thickness Uniformity



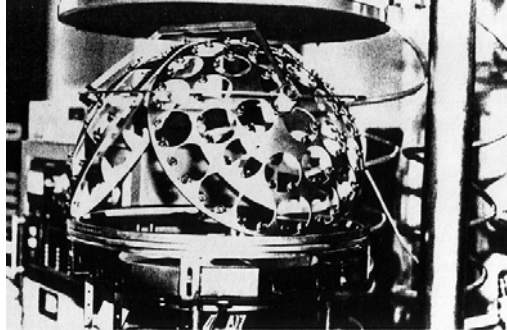
33

Evaporation Scheme to achieve Uniform Deposition



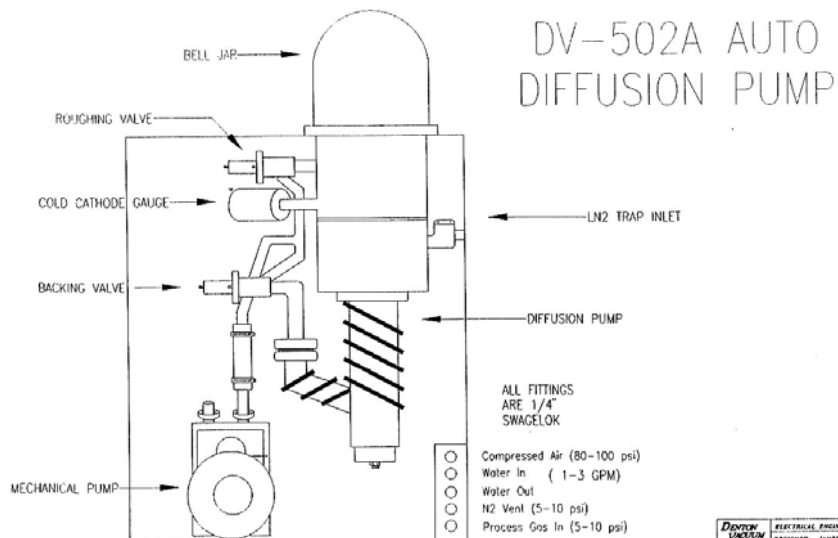
34

Evaporator



35

Evaporator



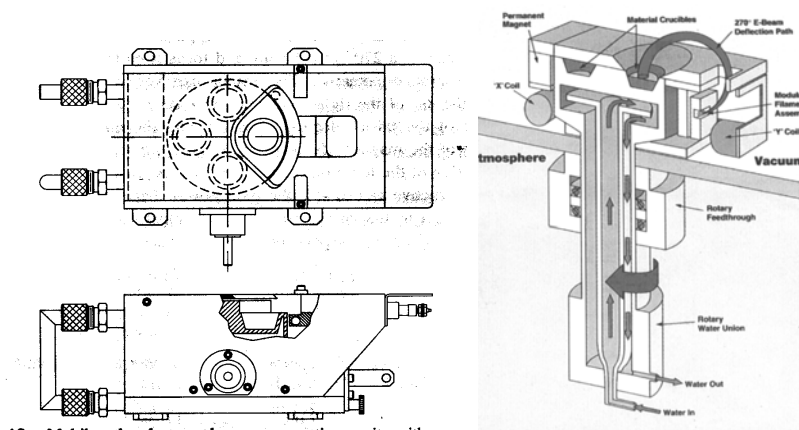
DETCON VACUUM	ELECTRICAL ENGINEERING DEPT.
DESIGNER: JAMES FALCO	DATE: 8-22-88
DV-502A REAR VIEW	FILE: HANNAH
AUTO DIFFUSION PUMP	1/4\"
REV: 1	8/22/88
A-0125-103-000	5/

電子束蒸鍍 (E-Beam Evaporation)

- ❑ 可聚焦的電子束，能局部加溫元素源，因不加熱其他部分而避免污染。
- ❑ 高能量電子束能使高熔點元素達到足夠高溫以產生適量的蒸氣壓。

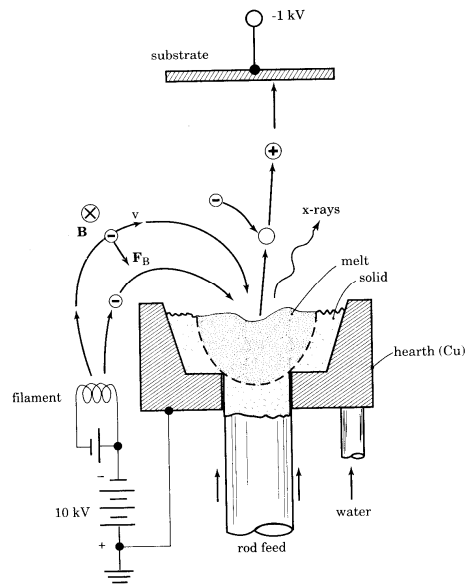
37

E-Gun Source



38

電子束與蒸發源之作用



39

Beam-Material Interactions and Operations

- A raster scan of e-beam improves the film uniformity.
- For an accurate deposition-rate control, vapor flux monitoring with feedback control to the filament power should be used.
- The macroparticle-spitting problem may be minimized by using a vacuum-melted charge.
- The energies of vapor atoms are about 0.2 eV.
- X-rays are emitted and may cause charge-trapping defects in the dielectric materials.
- The fraction of vapor ionized varies with beam density and vapor composition and is of the order of 0.01 to 0.1. A negative bias may be applied to the substrate to affect the film structures – so-called Ion Plating.

40

量產型電子束蒸鍍系統



41

E-Beam NCKU



最小均勻薄膜厚度100Å, 提供靶材金、鋁、鎳、鉑、鈦、鉻、銀

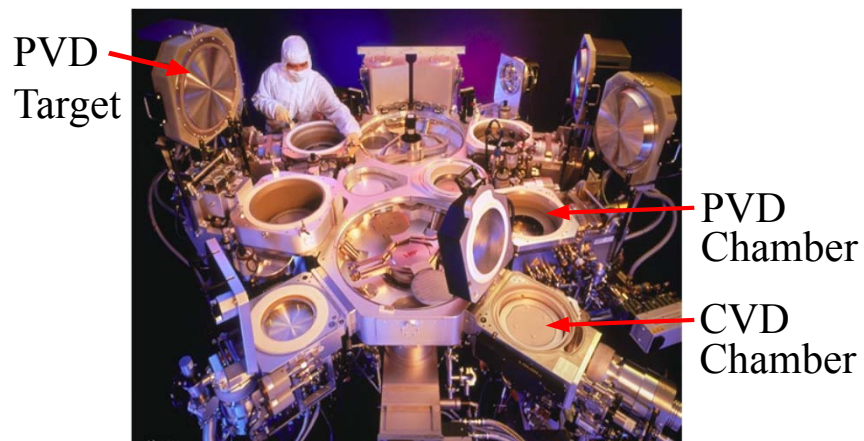
42

E-Beam NCKU



最小均勻薄膜厚度100Å, 提供靶材金、鋁、鎳、鉑、鈦、鉻、銀 43

Endura[®] PVD System



44

Sputtering

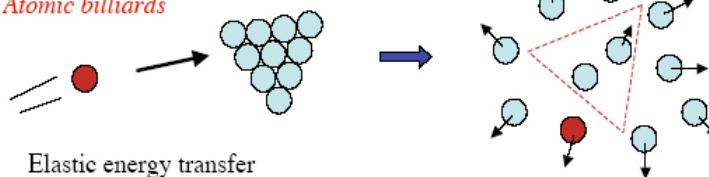
- The major PVD method in silicon technology
- Using ion bombardment to introduce mass transfer
- Basically, it is a low temperature process
 - can deposit virtually any materials, including metals, ceramics, and organic materials
 - can deposit composite film with controllable composition
- Substrate damage is the major disadvantage

45

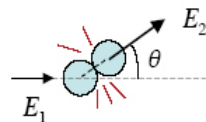
Sputtering process

Ar⁺ impact, momentum transfer at **cathode** ⇒ e⁻ avalanche and released target atoms, ions.

Atomic billiards



Elastic energy transfer



$$\frac{E_2}{E_1} \propto \frac{4M_1M_2}{(M_1 + M_2)^2} \cos^2 \theta$$

E_2 greatest for $M_1 \approx M_2$

For e⁻ hitting anode, substrate, $M_1 \ll M_2$

$$\frac{E_2}{E_1} \approx \frac{4M_1}{M_2} \quad (\text{small})$$

But e⁻ can give up all its E_K in **inelastic** collision:

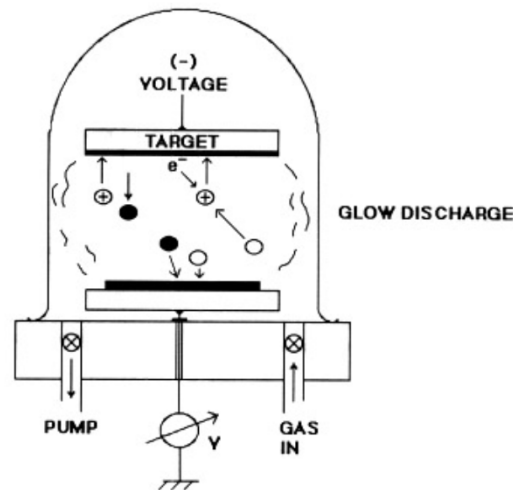
$$\frac{1}{2} m_e v_e^2 \Rightarrow \Delta U$$

Excitation of atom or ion

46

Sputtering

- Argon (Ar) atoms is usually used as the ion source



47

Sputtering yield

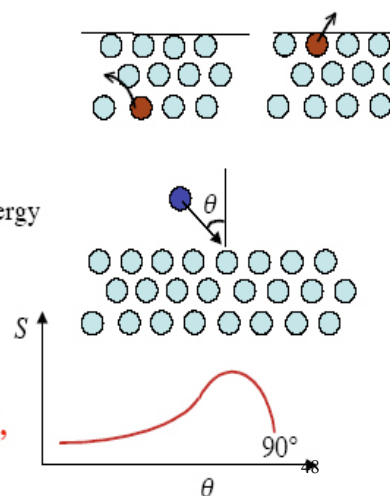
$$S = \text{Sputtering yield} = \frac{\text{\# atoms, molecules from target}}{\text{\# incident ions}}$$

$$S = \sigma_0 n_A \frac{\bar{E}_2}{4 E_{\text{thresh}}} \times \left[\dots \sqrt{\ln \left(\frac{E_v}{E_b} \right)} \right]$$

πd^2 # / area # excited in each layer Random walk to surface; E_b = binding energy

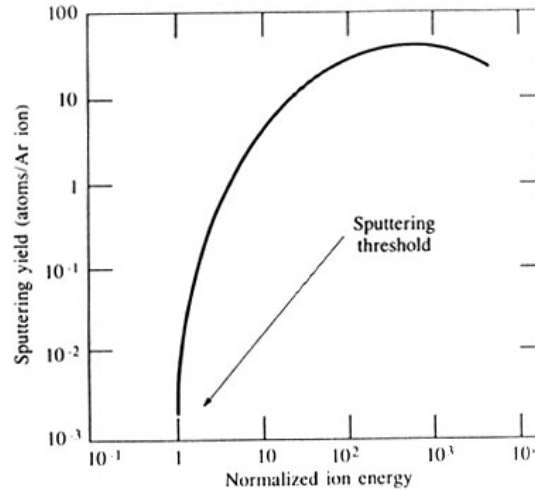
Oring, Fig. 3.18,
Table 3-4

Sputter rate depends on angle of incidence, relative masses, kinetic energy.



Sputtering Yield

- See Campbell 12.8 for detail
- Each material has its sputtering threshold
– $\sim 10 - 30$ eV
- Ion energy \uparrow , sputtering yield \uparrow
- However, if energy is too high, it becomes “ion implantation”



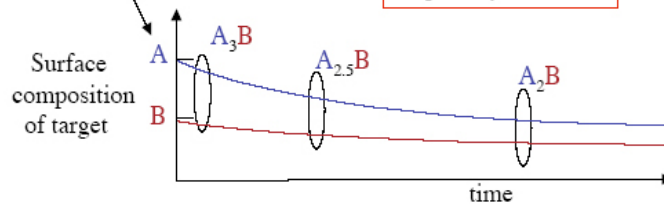
49

Target composition vs. film composition

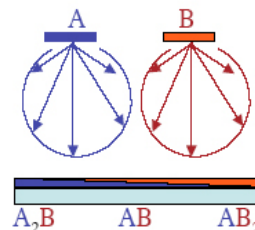
Sputtering removes outer layer of target \Rightarrow more uniform composition
(problem with multicomponent system only initially)

Initial target composition A_2B (e.g. Si_2W)

If sputter yield $A > B$



Co-sputtering \Rightarrow composition control,
sample library



50

Sputtering vs. Evaporation

- Temperature
 - evaporation requires high temperature
- Materials
 - evaporation: metal only
 - sputtering: virtually any materials
- Sputtering has better step coverage
- contamination problem
- Sputtering results more severe damage in substrate than evaporation

51

Sputter



52

Magnetron Sputter NCKU



利用磁力控制增加電漿密度，以離子轟擊靶材濺鍍銅、鋅、鉻、鋁、鈦金屬薄膜(DC gun)與金屬氧化物氧化鋅、二氧化矽薄膜(RF gun)於基板上，最小均勻厚度200Å。 53

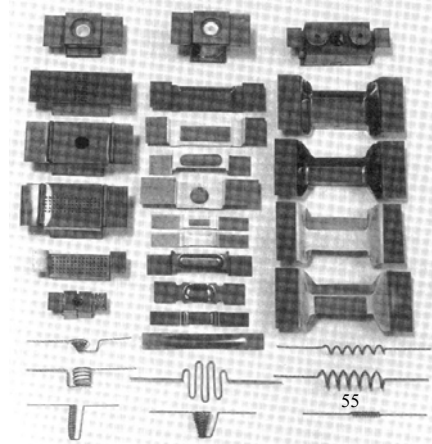
濺鍍(Sputtering) vs. 蒸鍍(Evaporation)

- Temperature
 - evaporation requires high temperature
- Materials
 - evaporation: metal only
 - sputtering: virtually any materials
- Sputtering has better step coverage
- contamination problem
- Sputtering results more severe damage in substrate than evaporation

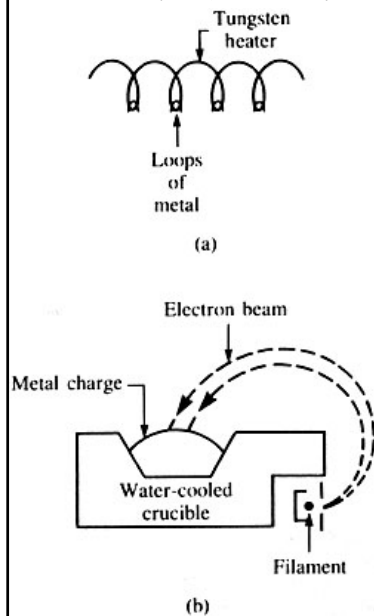
54

物理氣相沉積系統硬體

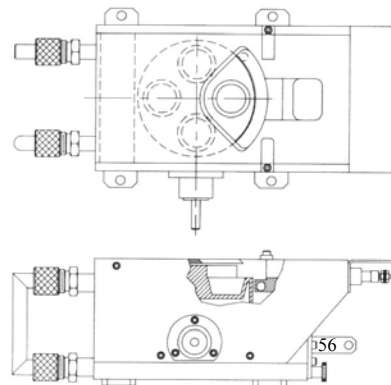
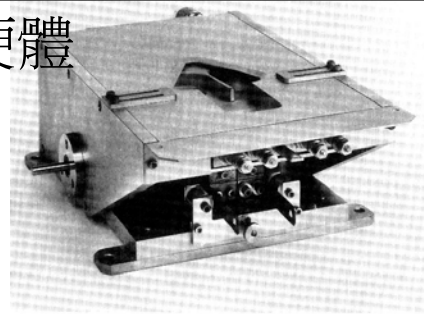
- Thermal Evaporator
 - Vacuum pump
 - Resistance heated evaporation sources
 - Sublimation furnaces, crucible sources
- E-beam evaporator
 - Vacuum pumps
 - Electron generators
 - Electrical bias system



物理氣相沉積系統硬體



E-beam evaporator



物理氣相沉積系統硬體

- Sputter
 - Vacuum pump
 - Power supply for DC or RF glow discharge
 - Sputtering gas source

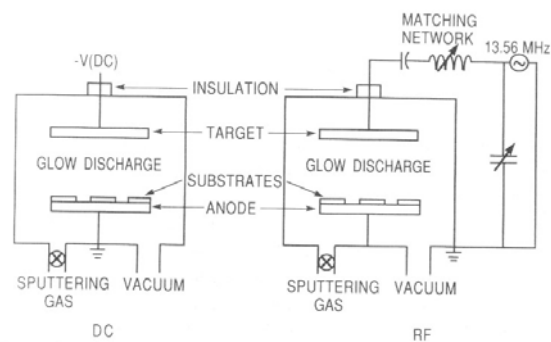
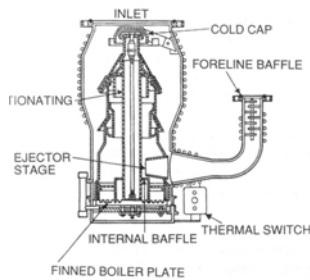
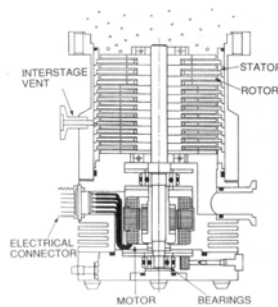


Figure 3-13. Schematics of simplified sputtering systems: (a) dc, (b) RF.

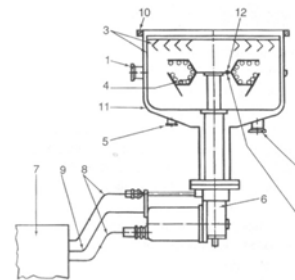
Vacuum Pumps



Diffusion pump



Turbomolecular pump



Cryopump

Vacuum Pumps

- Vacuum pumps



Turbo pump

Momentum transfer
Mass-dependent
compression ratio



Diffusion pump

Momentum transfer
Cracking & contamination



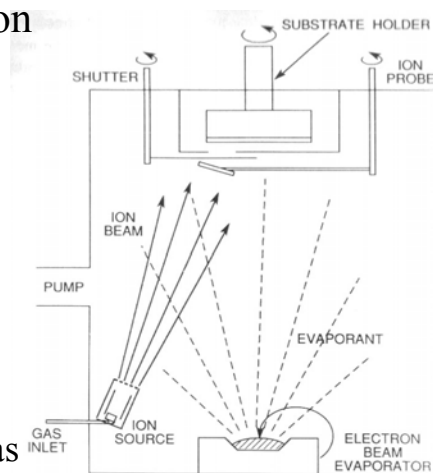
Cryopump

Trap molecules
Liquid Helium

59

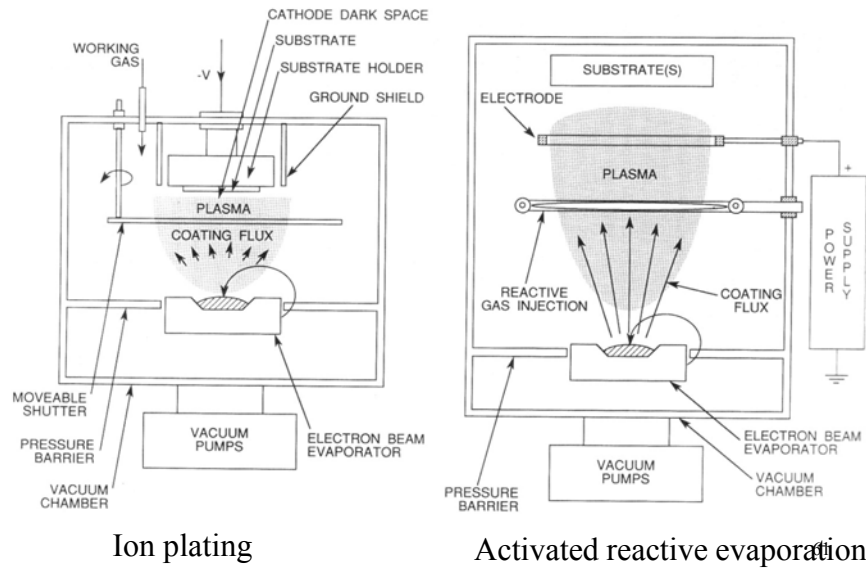
Hybrid and Modified PVD Processes

- Ion beam assisted deposition
 - Use ion bombardment to adjust PVD films
- Ion plating
 - Thermal evaporation + ion sputtering
 - Provide excellent adhesion
- Reactive evaporation
 - Metal vapor reacts with a gas to form compound deposits



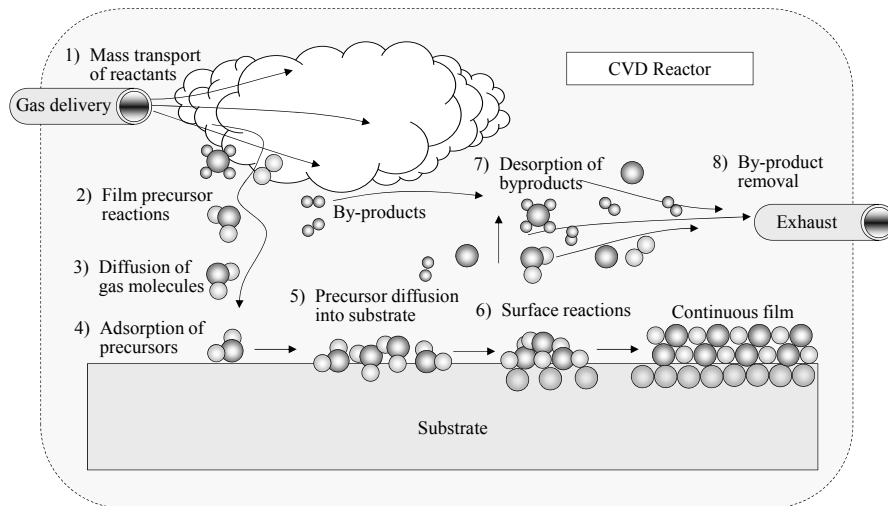
Ion-beam-assisted deposition

Hybrid and Modified PVD Processes



Part IV: Chemical Vapor Deposition

Schematic of CVD Transport and Reaction Steps

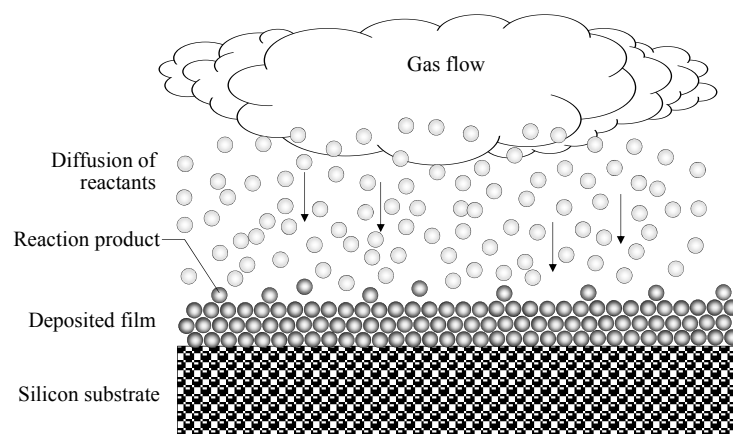


Semiconductor Manufacturing Technology
by Michael Quirk and Julian Serda

Figure 11.8

© 2001 by Prentice Hall

Gas Flow in CVD



Semiconductor Manufacturing Technology
by Michael Quirk and Julian Serda

Figure 11.9

© 2001 by Prentice Hall

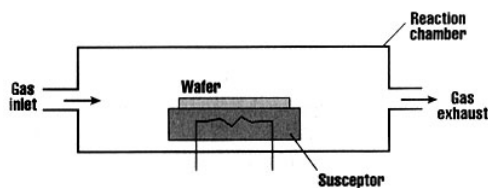
CVD Applications

	<i>FILMS</i>	<i>PRECURSORS</i>
Semiconductor	Si (poly) Si (epi)	SiH ₄ (silane) SiCl ₂ H ₂ (DCS) SiCl ₃ H (TCS) SiCl ₄ (Siltet)
Dielectrics	SiO ₂ (glass)	LPCVD SiH ₄ , O ₂ PECVD SiH ₄ , N ₂ O PECVD Si(OC ₂ H ₅) ₄ (TEOS), O ₂
		LPCVD TEOS APCVD&SACVD TM TEOS, O ₃ (ozone)
	Oxynitride	SiH ₄ , N ₂ O, N ₂ , NH ₃
	Si ₃ N ₄	PECVD SiH ₄ , N ₂ , NH ₃ LPCVD SiH ₄ , N ₂ , NH ₃ LPCVD C ₈ H ₂₂ N ₂ Si (BTBAS)
Conductors	W (Tungsten) WSi ₂ TiN Ti Cu	WF ₆ (Tungsten hexafluoride), SiH ₄ , H ₂ WF ₆ (Tungsten hexafluoride), SiH ₄ , H ₂ Ti[N(CH ₃) ₂] ₄ (TDMAT) TiCl ₄

65

Introduction to CVD

- Form thin films on the surface of a substrate by thermal decomposition and/or reaction of gaseous compounds
- Usually performed at high temperature
 - Can be performed at various pressure and with assistance of plasma
 - Usually at viscous flow regime



66

Type of CVD

- APCVD
 - high deposition rate, poor uniformity, high contamination level, 250-450 °C
 - for dielectrics
- LPCVD
 - low deposition rate, high uniformity, 575-650 °C
 - for polysilicon
- PECVD
 - for extremely low deposition temperature
 - e.g, oxide and nitride
 - quality is poor

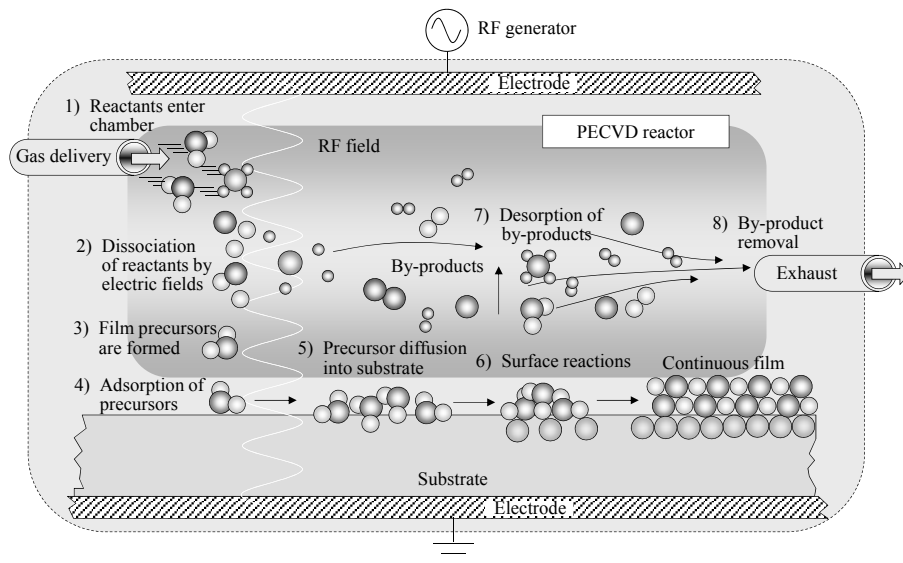
67

Plasma Enhanced CVD

- PECVD with SiH₄ and NO₂ (laughing gas)
$$\text{e}^- + \text{SiH}_4 \rightarrow \text{SiH}_2 + 2\text{H} + \text{e}^-$$
$$\text{e}^- + \text{N}_2\text{O} \rightarrow \text{N}_2 + \text{O} + \text{e}^-$$
$$\text{SiH}_2 + 3\text{O} \rightarrow \text{SiO}_2 + \text{H}_2\text{O}$$
- Plasma enhanced chemical reaction
- PECVD can achieve high deposition rate at relatively lower temperature

68

Film Formation during Plasma-Based CVD

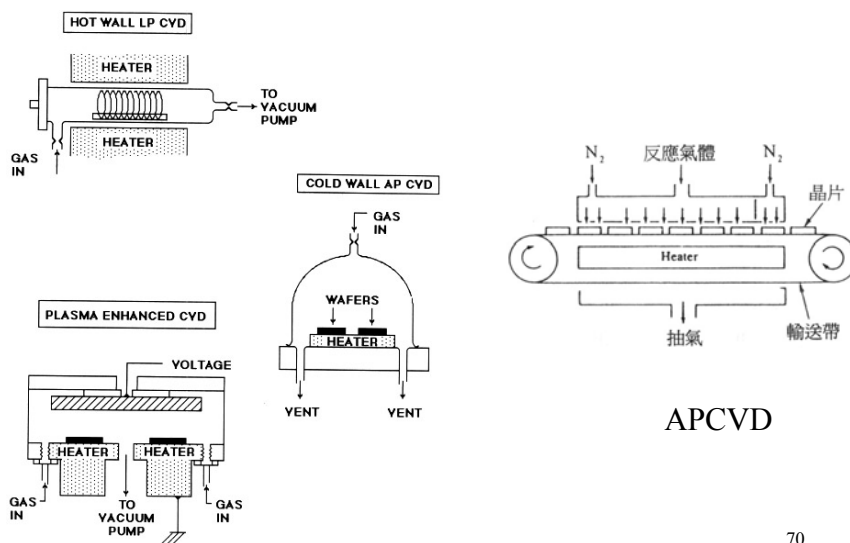


Semiconductor Manufacturing Technology
by Michael Quirk and Julian Serda

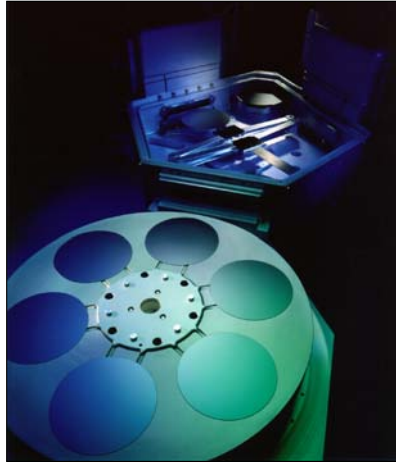
Figure 11.19

© 2001 by Prentice Hall

CVD Reaction Chambers



Chemical Vapor Deposition Tool



Photograph courtesy of Novellus, Sequel CVD

Semiconductor Manufacturing Technology
by Michael Quirk and Julian Serda

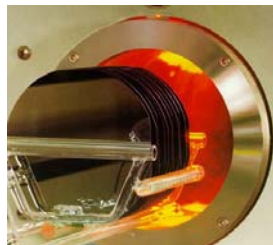
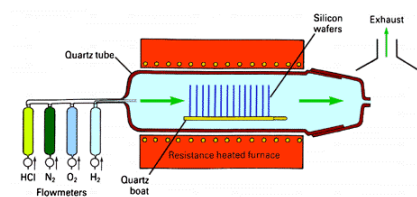
Photo 11.3

© 2001 by Prentice Hall

STATE UNIVERSITY OF NEW YORK at STONY BROOK

Yu-Hsuan Su

Furnace Tubes Thermal Oxidation and LPCVD



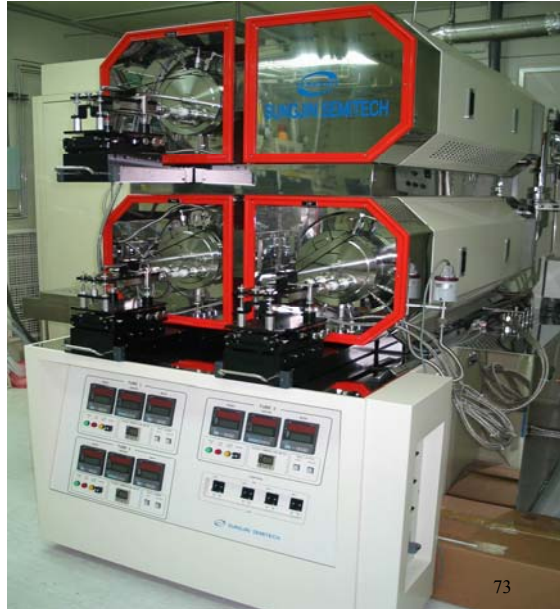
72

LPCVD NCKU

(1)多晶矽，主要氣體
SiH₄、N₂；溫度約
620°C、壓力約300
mtorr，

(2)低應力氮化矽，主
要氣體 NH₃、DSC、
N₂；溫度約810°C、
壓力約200 mtorr

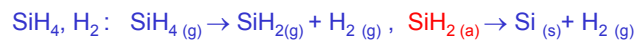
(3)低溫氧化矽，主要
氣體 SiH₄、O₂、N₂；
溫度約450°C、壓力約
200 mtorr



STATE UNIVERSITY OF NEW YORK at STONY BROOK

Poly-Silicon LPCVD

Process:



pressure :~500 mtorr.

temp.: 540 °C(amorphous) ~ 650 ° C (polysilicon, 0.03~0.3μm)

dep. rate: 100~200 Å/min.

Conducting poly-Si: PH₃, AsH₃ reduce deposition rate, while B₂H₆ increases deposition rate.

Conformality: spectacular, excellent uniformity

Compatibility: thermal issue (no Al)

SiH₄ is extremely toxic gas and extremely *flammable* at atmospheric pressure ! (Osaka University)

amorphous Si (solar cells & TFT), poly-Si (LCD projector)

74

LTO LPCVD

Process:

SiH_4 , O_2 (PH_3 , AsH_3 , B_2H_6) Note SiH_4 and O_2 at 350 mtorr. will not explode!

pressure : ~350 mtorr. temp.: 300 °C ~ 400 °C

dep. rate: 175 Å/min. for 4" wafer

Planarization → flow → add dopants to reduce melting point

PSG (Phosphosilicate): phosphine doped LTO

(6~8% wt., 1000 °C), "gettering" of small positive ions

BPSG (Borophosphosilicate):

(4~5% wt. each, 900°C), contact dielectrics

flow process may be replaced by CMP planarization

Conformality: not so good (P glass reflow)

Compatibility: good for Aluminum

PH_3 , AsH_3 , B_2H_6 are all extreme toxic gases, usually shipped with SiH_4 for safety (explosive alert!).

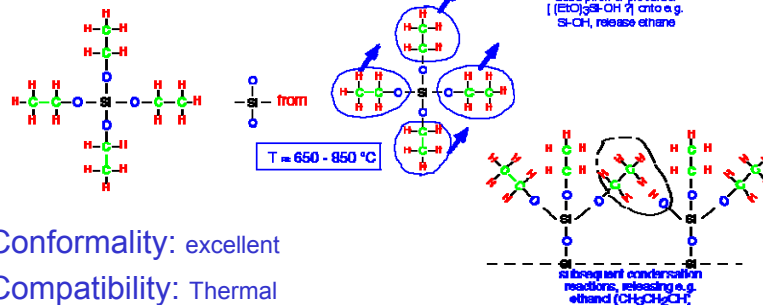
75

TEOS LPCVD

Process:

$\text{TEOS} \rightarrow \text{SiO}_2 + 2\text{C}_2\text{H}_4 + 2\text{CH}_3\text{CH}_2\text{OH}$

pressure : 1 atm, N_2 temp.: 650~850 °C



Conformality: excellent

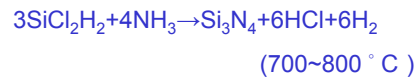
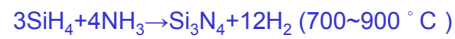
Compatibility: Thermal

76

Nitride LPCVD

Process:

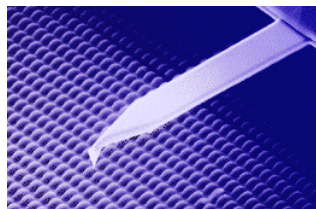
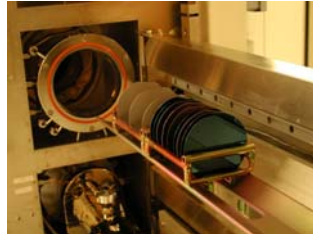
SiH₄, SiCl₂H₂, NH₃
 pressure :~250 mtorr. temp.: ~ 800 ° C
 dep. rate: 35 Å/min. for 4" wafer
 1100MPa tensile stress



Conformality: good to excellent

Compatibility: Thermal

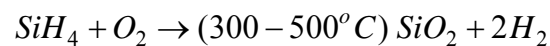
Olympus, Tetra Tip



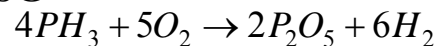
77

Oxide

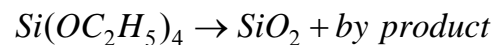
- Silane based



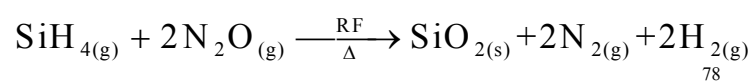
- PSG



- TEOS (LPCVD 650-700 °C) (PECVD 350 °C)



- Silane based PECVD



78

Oxide

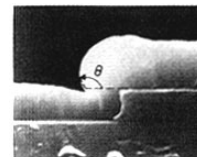
Table 6.1 Properties of Various Deposited Oxides. (After ref. [2].)

Source	Deposition Temperature (°C)	Composition	Conformal Step Coverage	Dielectric Strength (MV/cm)	Etch Rate (Å/min) [100:1 H ₂ O:HF]
Silane	450	SiO ₂ (H)	No	8	60
Dichlorosilane	900	SiO ₂ (Cl)	Yes	10	30
TEOS	700	SiO ₂	Yes	10	30
Plasma	200	SiO _{1.7} (H)	No	5	400

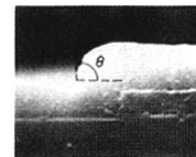
79

PSG and BPSG

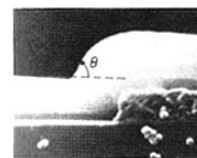
- Phosphosilicate glass (PSG)
 - reducing stress
 - improve step coverage
 - flow at high temperature (1000-1100 °C) to create smooth surface
- Borophosphosilicate glass (BPSG)
 - flow temperature is reduced to 700 °C
 - for isolation and surface planarization



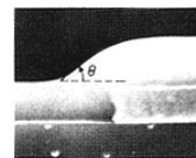
(a) 0% P



(b) 2.2%



(c) 4.6%

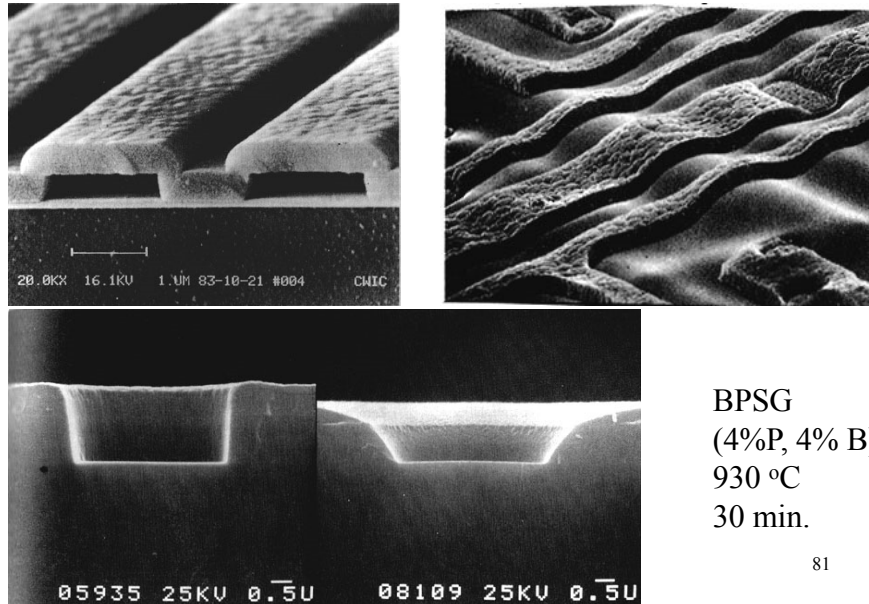


(d) 7.2%

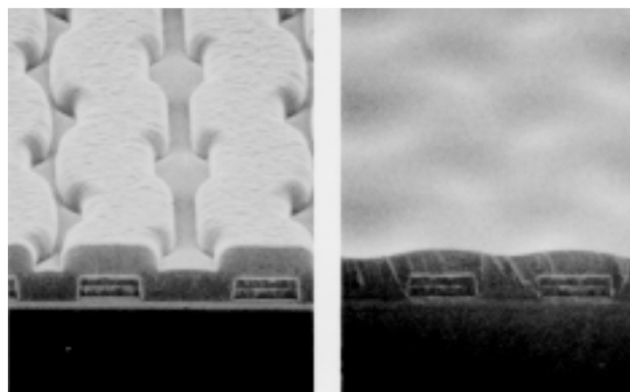
1100 °C for 20 min.

80

Reflow of BPSG



4×4 BPSG Reflow at 850 °C, 30 Minutes in N₂ Ambient



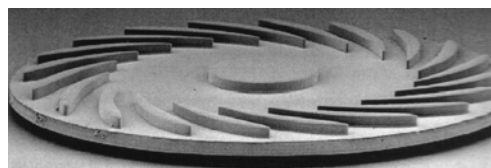
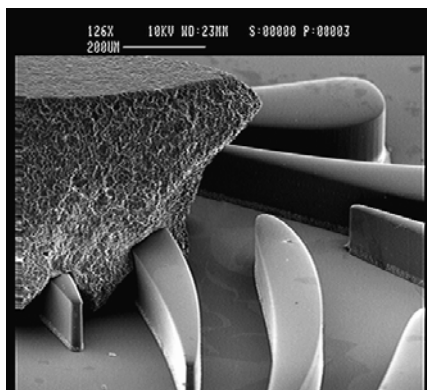
82

Silicon Nitride

- Application:
 - masks to prevent oxidation for LOCOS process
 - final passivation barrier for moisture and sodium contamination
- APCVD
$$3\text{SiH}_4 + 4\text{NH}_3 \rightarrow \text{Si}_3\text{N}_4 + 12\text{H}_2$$
- LPCVD
$$3\text{SiCl}_2\text{H}_2 + 4\text{NH}_3 \rightarrow \text{Si}_3\text{N}_4 + 6\text{HCl} + 6\text{H}_2$$

83

CVD SILICON CARBIDE PROCESSING



Procedure:

1. DRIE silicon substrate
2. Wafer bonding
3. CVD silicon carbide
4. Etch silicon

Lohner, MIT 1999

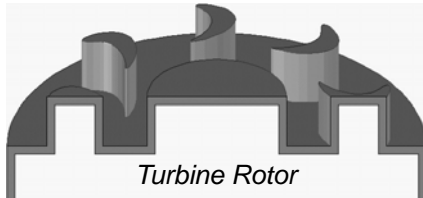
84

BASIC PROCESSING CONCEPTS

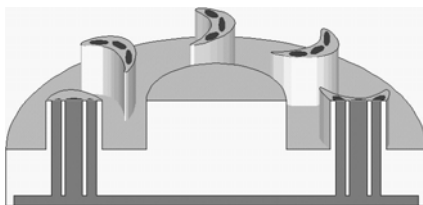
Key:  = SiC  = Si

Positive Mold

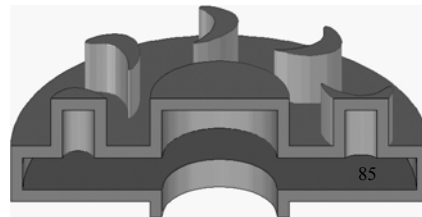
Negative Mold



Hybrid Structure

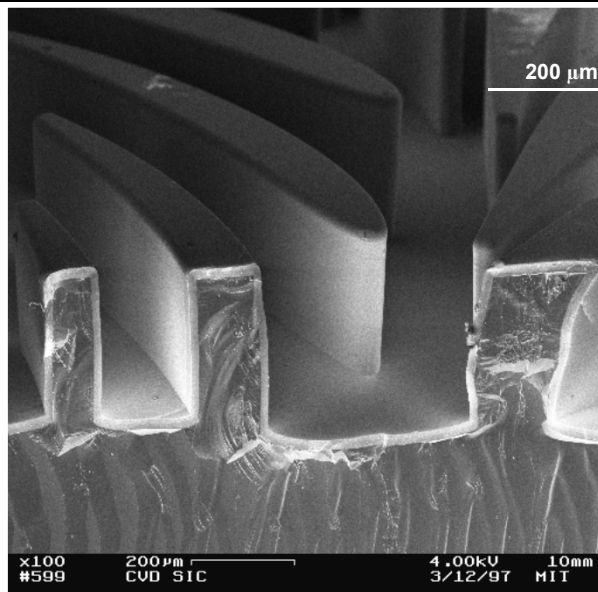


Dissolve Silicon ↓



POSITIVE MOLD RESULTS

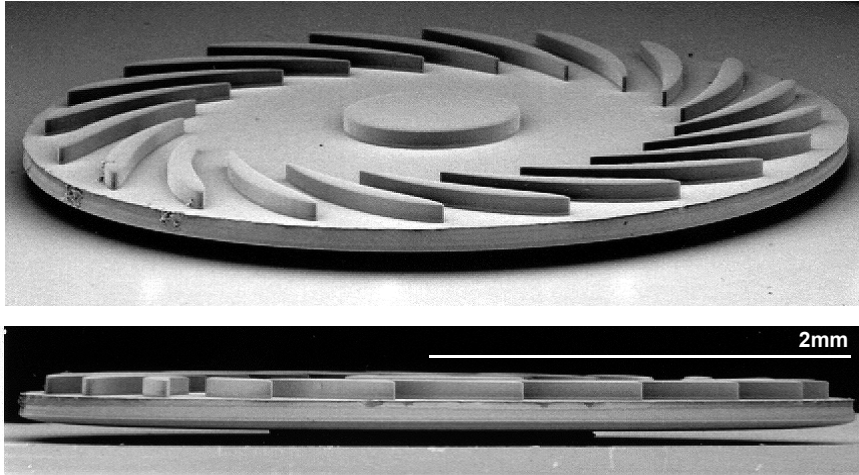
16μm THICK SiC COATING



86

NEGATIVE MOLD RESULTS

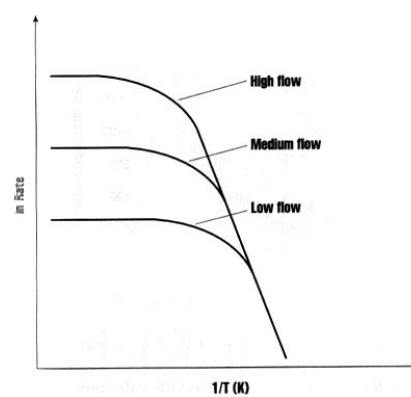
50 μm THICK SHELL SILICON CARBIDE ROTOR



87

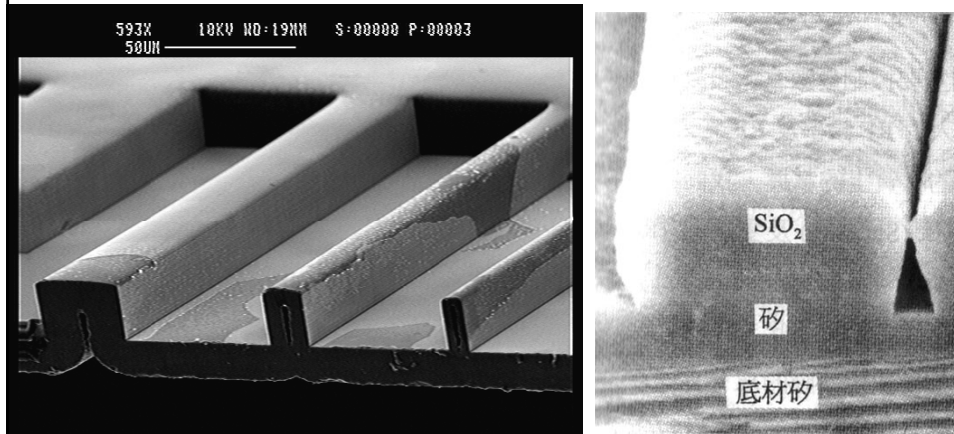
CVD Rate Control

- Typical CVD deposition rate is shown
 - at lower temperature, surface reaction is the control mechanism
 - increase temperature
 - at higher temperature, bulk transportation is the control mechanism
 - increase flow rate



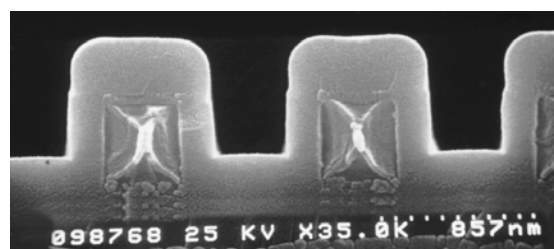
88

Step Coverage

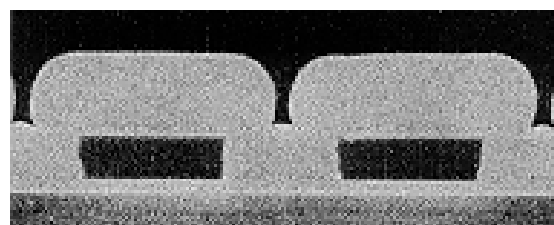


89

Step Coverage of TEOS and Silane Oxide



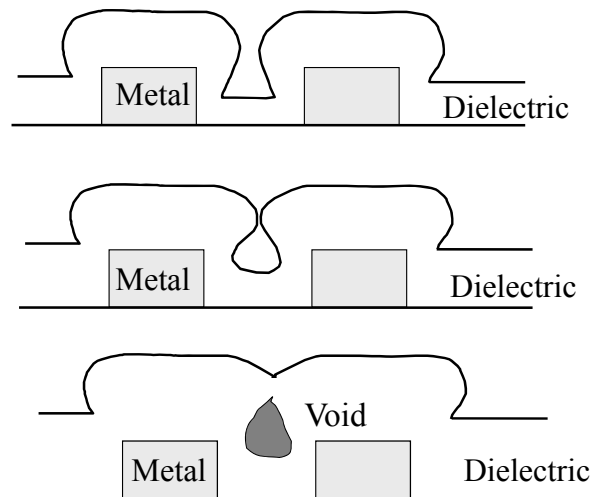
TEOS



Silane

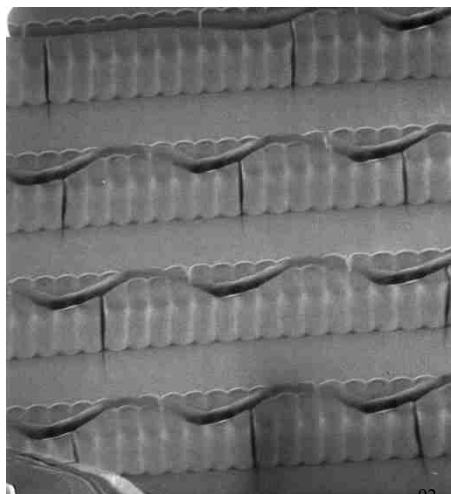
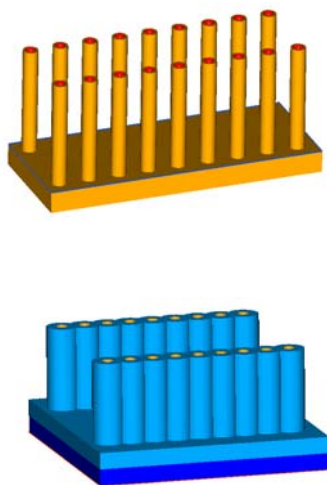
90

Void Formation Process



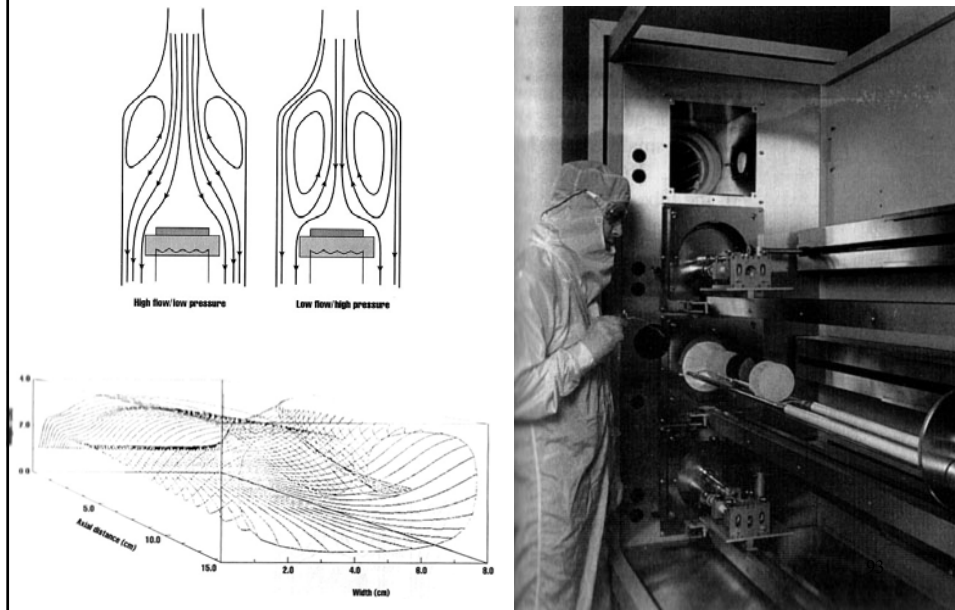
91

CVD Example: Micro Heat Exchanger



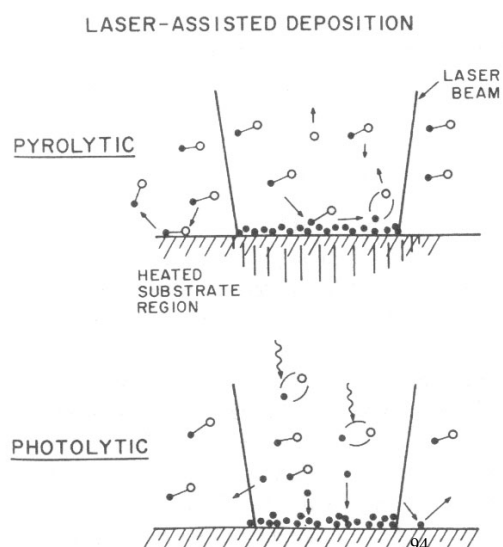
92

CVD Reactors



Laser Assisted Deposition

- Pyrolytic
 - By heating substrate to enhance reactivity
- Photolytic
 - Direct dissociation of molecules by energetic photons



LCVD

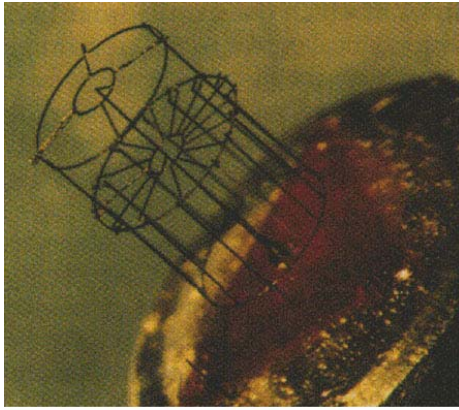


Figure 6. A 3-dimensional structure of alumina grown with the LCVD process.

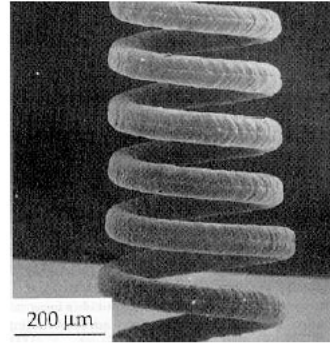


Figure 7. Tungsten coil made by LCVD.

95

Part V: Epitaxial Growth

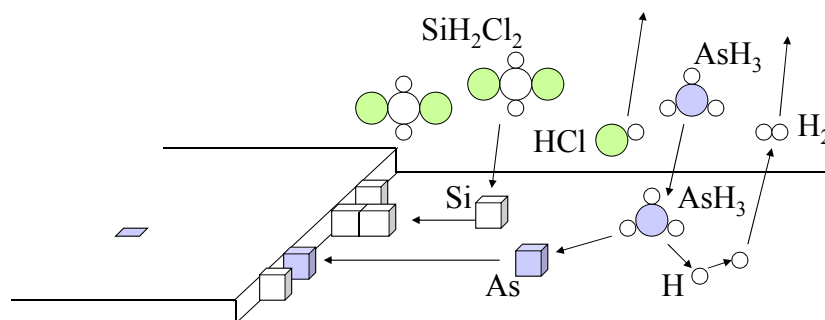
96

Epitaxy

- Deposition of single crystal films on single crystal substrate
 - Single silicon acts as a seed
 - This is called vapor phase epitaxy (VPE)
 - Other technologies include liquid phase epitaxy (LPE) and molecular beam epitaxy (MBE), they are widely used in GaAs process
- To grow single crystal n-type layers on p-type substrates for bipolar processing
- Prevent CMOS latchup

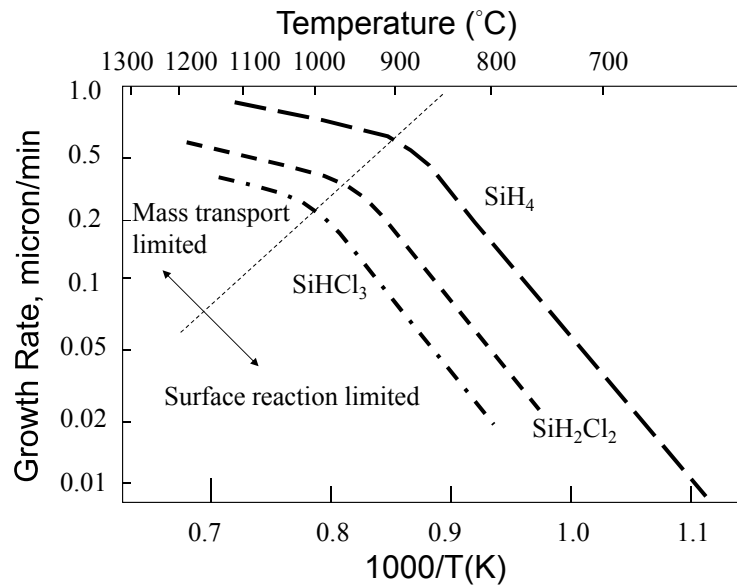
97

Schematic of DCS Epi Grow and Arsenic Doping Process



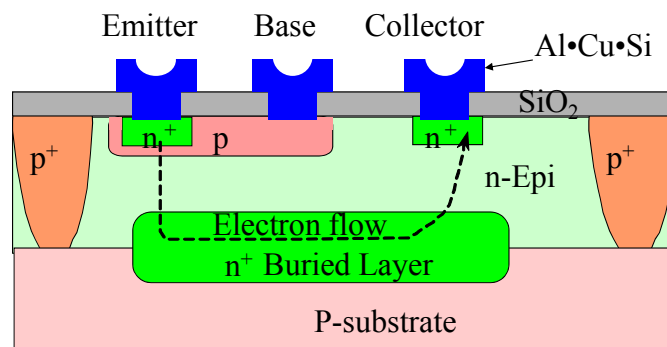
98

Epitaxial Silicon Growth Rate Trends



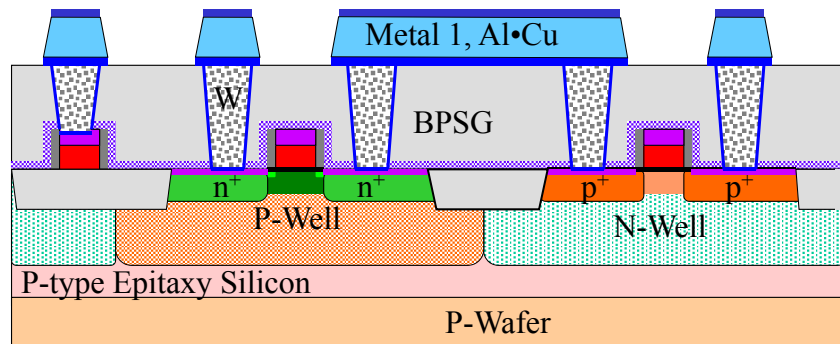
99

Epitaxy Application, Bipolar Transistor



100

Epitaxy Application: CMOS



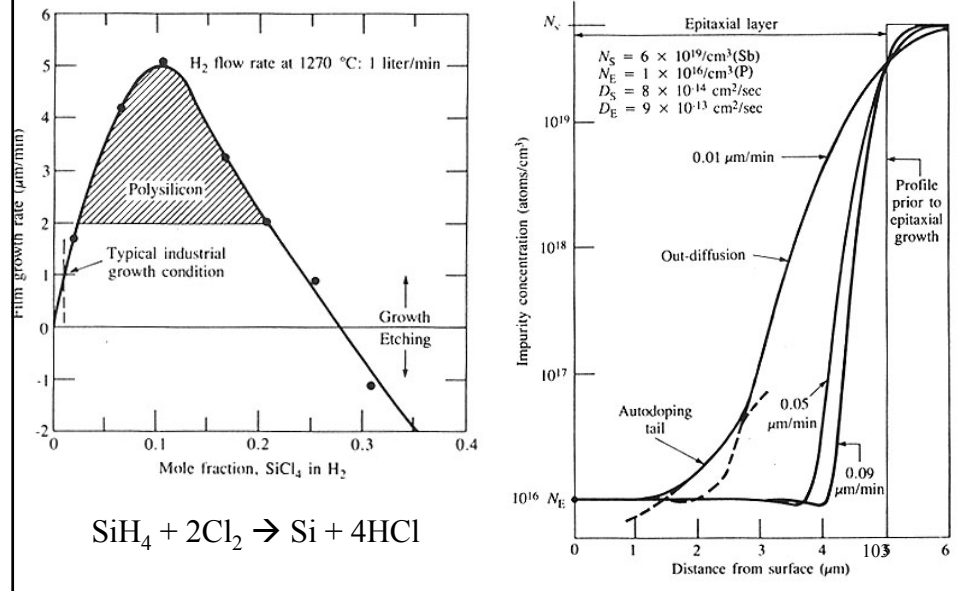
101

VPE (Vapor Phase Epitaxy)

- Single silicon from single silicon
- Use the same raw materials as polysilicon CVD
- Careful control is required to prevent polysilicon formation
- Resistivity can be adjusted by doping
- Buried layer
 - Heavily doped layer first, then epitaxy layer
 - Auto doping
 - Out-diffusion

102

VPE



LPE and MBE

- LPE:
 - Substrate is brought into contact with a solution containing the material to be deposited in liquid form
 - Material crystallizes directly from the solute
 - 0.1 – 1 μm/min.
- MBE:
 - Crystalline layer is formed by deposition from a thermal beam of atoms or molecules
 - Deposition is performed in ultrahigh-vacuum condition (10⁻⁸ Pa)
 - 0.001-0.3 μm/min

CVD Vs. Epitaxy

- Crystalline
 - CVD: Polysilicon or amorphous
 - Epitaxy: single crystal
- Fluid dynamics
 - CVD: viscous flow regime
 - Epitaxy: molecular flow regime
- Technology
 - CVD: quite popular in Si technology
 - Epitaxy: mostly used in GaAs and BJT technology

105

Part VI: Thin Film Materials Sciences

106

Step Coverage

- A primary limitation of evaporation
 - material beams are nondivergent
- Need wafer rotation to improve step coverage
- Performance index
 - AR (step height/step diameter)
 - OK for $AR < 0.5$
 - marginal $0.5 < AR < 1$
 - poor if $AR > 1$

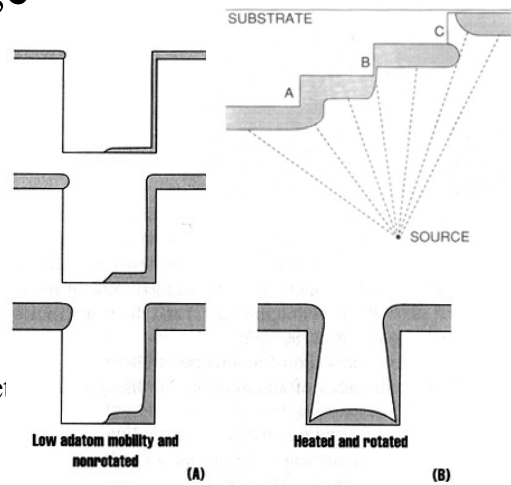
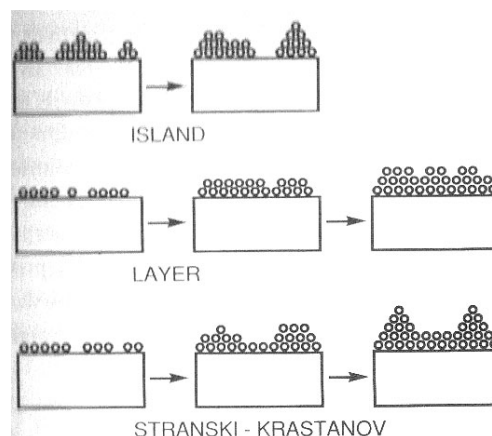


Figure 12-5 (a) Time evolution of the evaporative coating of a feature with aspect ratio of 1.0, with little surface atom mobility (i.e., low substrate temperature) and no rotation. (b) Final profile of deposition on rotated and heated substrates.

107

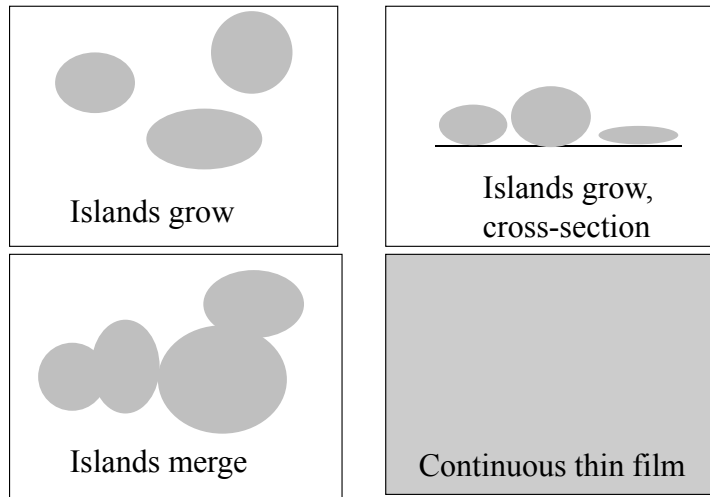
Thin Film Growth Modes

- Island (Volmer-Weber)
 - Film materials have stronger bonds
 - E.g., metals on insulators
- Layer (Frank-Van der Merwe)
 - Film / substrate has stronger bonds
 - E.g., epi of semiconductor
- Stranski-Krastanov



108

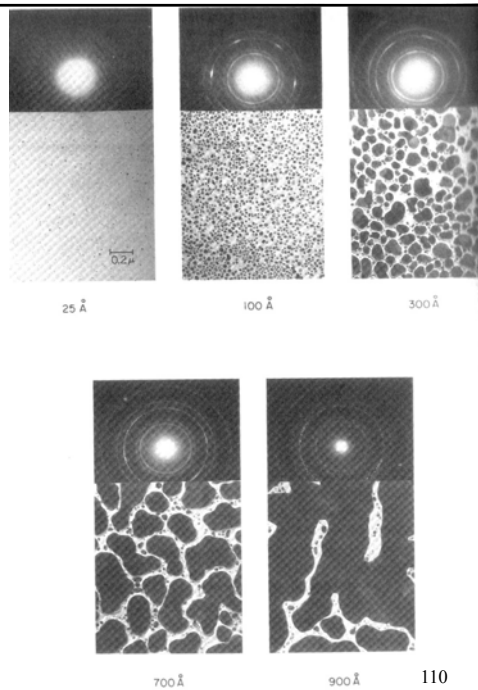
Deposition Process



109

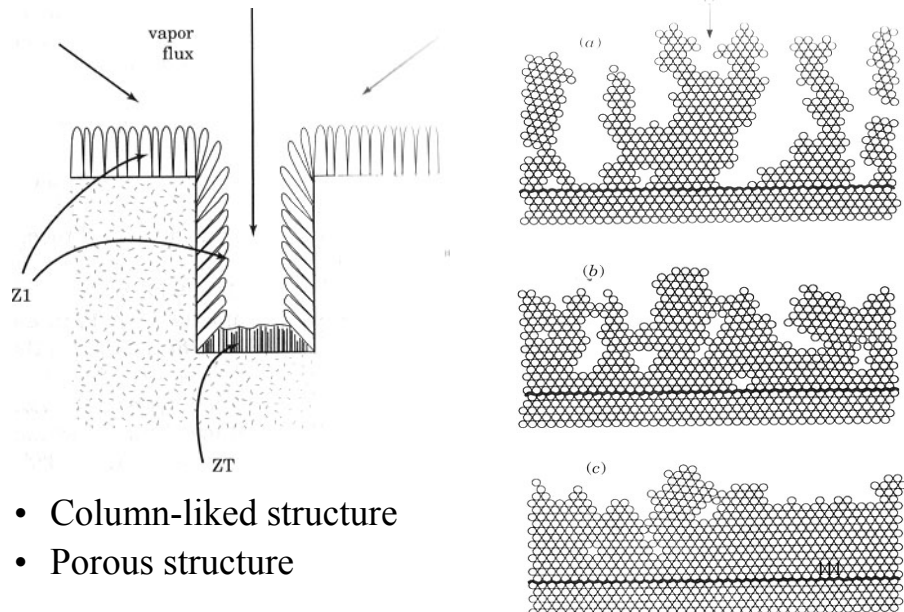
MicroStructures of PVD Films

- Microstructures are thickness and deposition period dependent
- E.g., Ag film on NaCl substrate

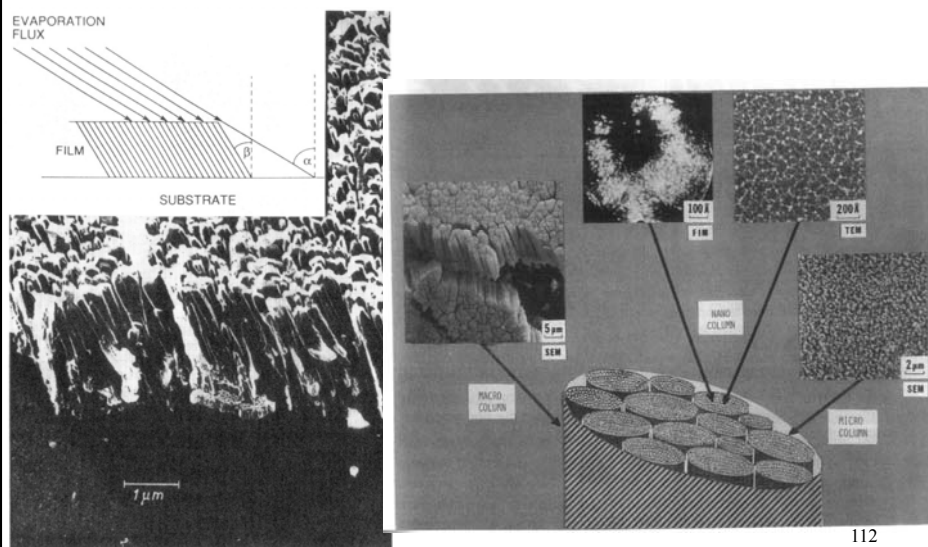


110

Micro Structures

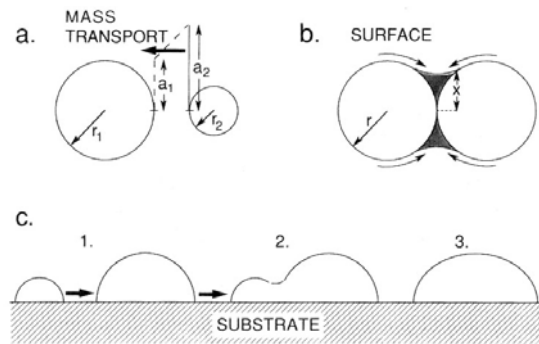


Micro and Nanostructure of Films



112

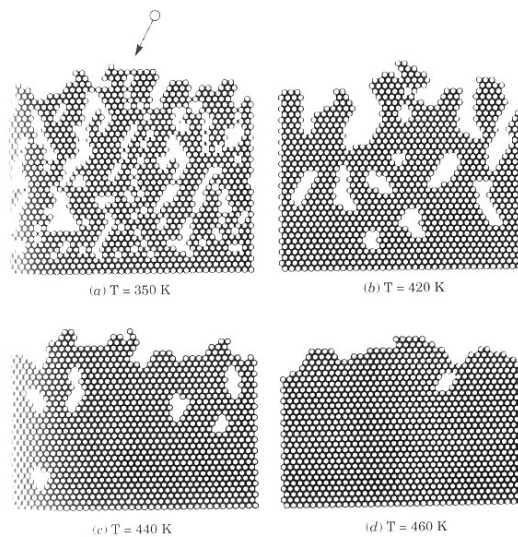
Coalescence of Clusters and Stress Generation



- Cluster migration causes the elimination of grain boundary and induces tensile strains

113

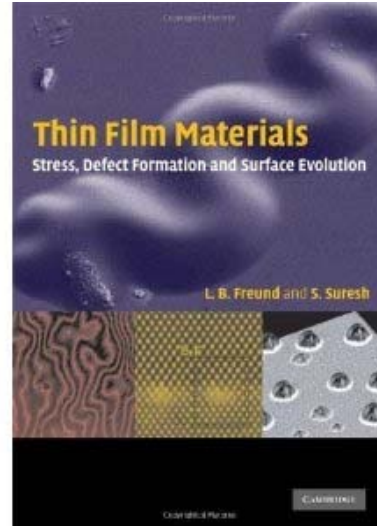
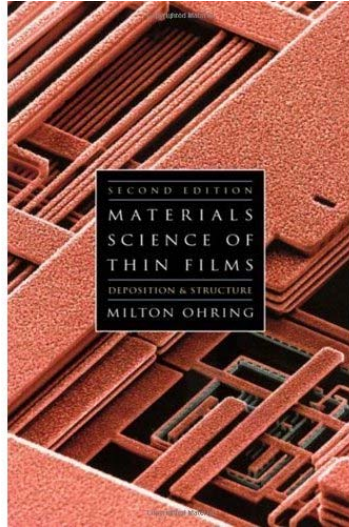
Molecular Dynamics Simulation



Low substrate temperature will result in porous structure

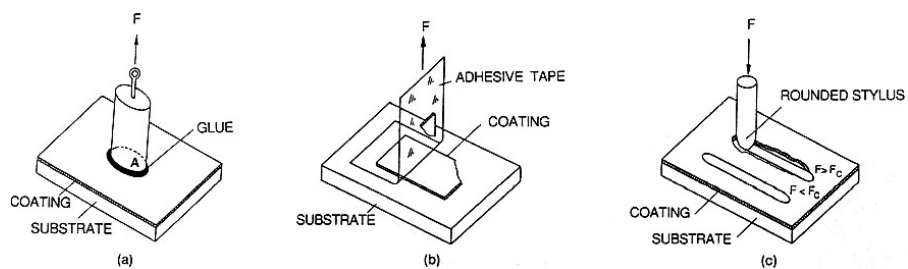
114

Two Good Textbooks



115

Adhesion

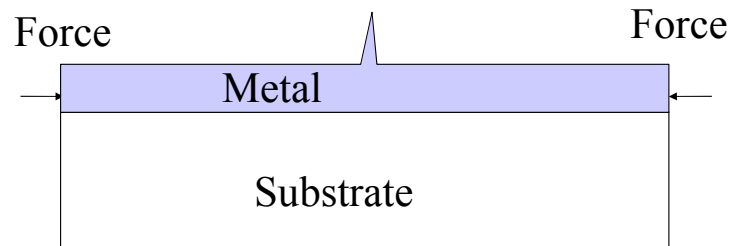


(a)pull test, (b)shear test, (c)scratch test

- Metal film must have enough adhesion force to substrate

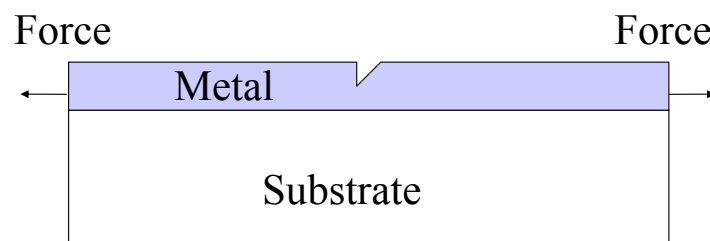
116

Compressive Stress Causes Hillock



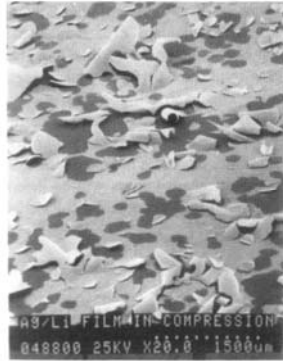
117

Tensile Stress Causes Crack

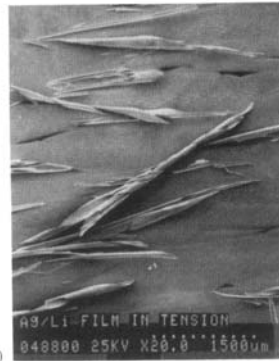


118

Failure of Thin Films



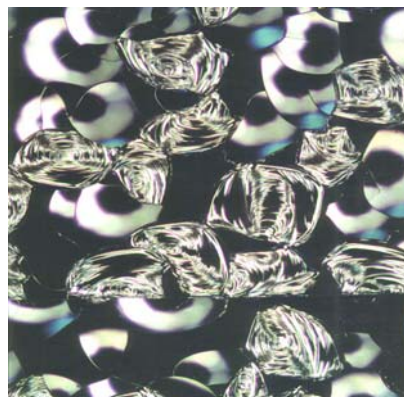
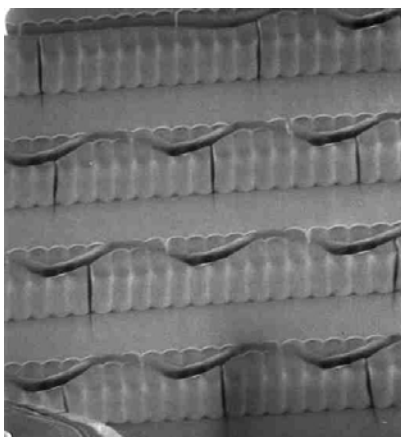
- Compressive failure
 - Bulking
 - Hillocks formation



- tensile failure
 - Cracking

119

Failure of Oxide Films



120

Microstructures

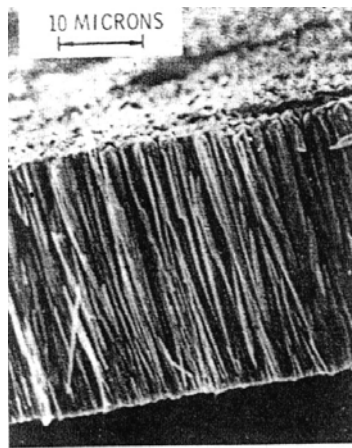
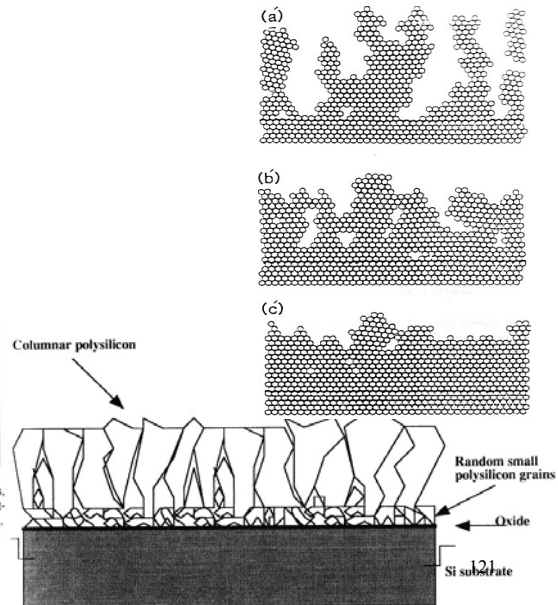


FIGURE 20. Scanning electron micrograph showing the porous, zone 1-type microstructure of Cr film deposited by magnetron sputtering at about 80°K. (From Thornton, J. A., J. Vac. Sci. Technol., 11, 268, 1974. With permission.)



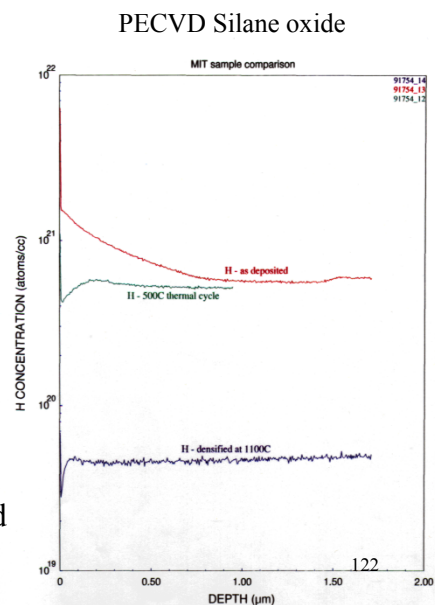
Hydrogen in PECVD Films

Table I. Hydrogen concentration of PECVD TEOS oxide.

T (°C)	350	700	800	900	1000	1100
H ₂ (%)	4.00	0.67	0.13	0.20	0.17	0.17

SIMS and RBS analysis to find hydrogen concentration in oxide

PECVD Silane oxide
H ~ 10% as deposited



Stoney Formula

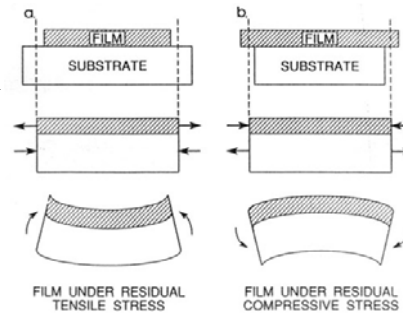
- Simple 1-D thermal stress

$$\sigma = (\alpha_f - \alpha_s)(T_1 - T_2)E_f / (1 - \nu_f)$$

- Stoney formula for thin film material

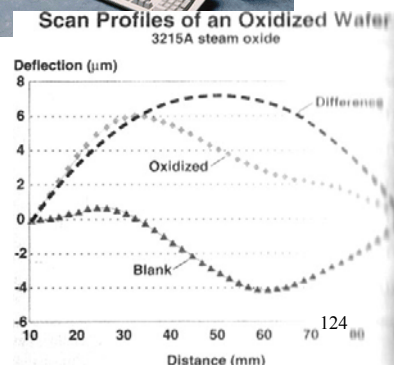
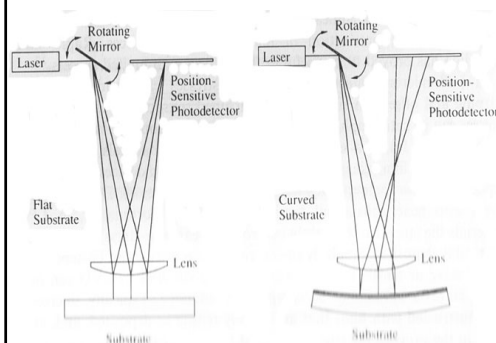
– by curvature measurement to find residual stress

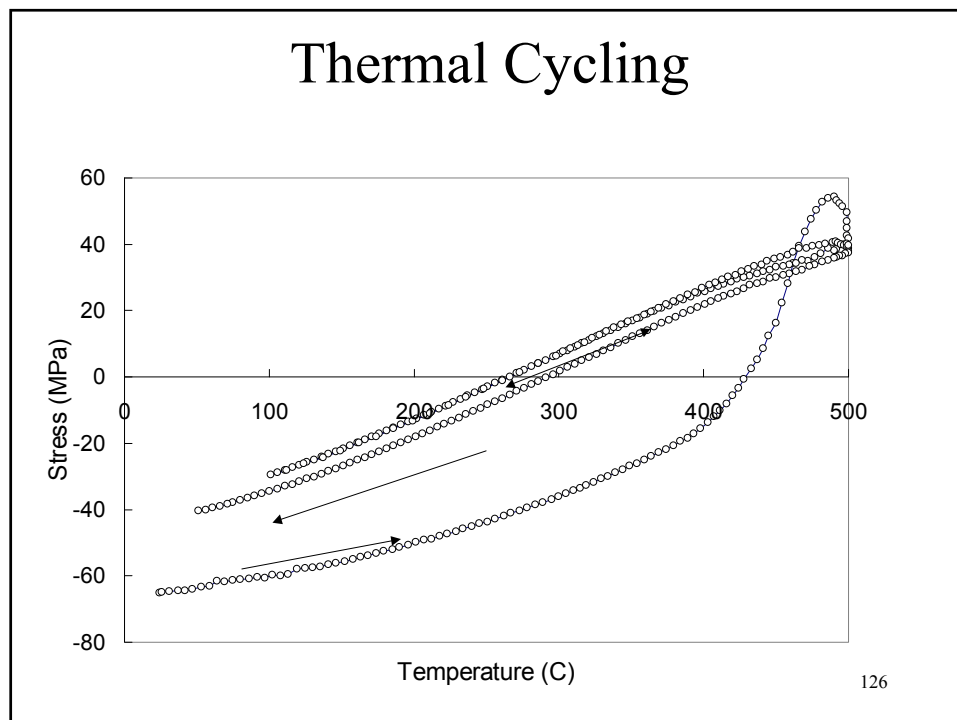
$$\sigma_f = \frac{E_s h_s^3}{6(1 - \nu_s) R h_f^2 (1 + h_s/h_f)}$$



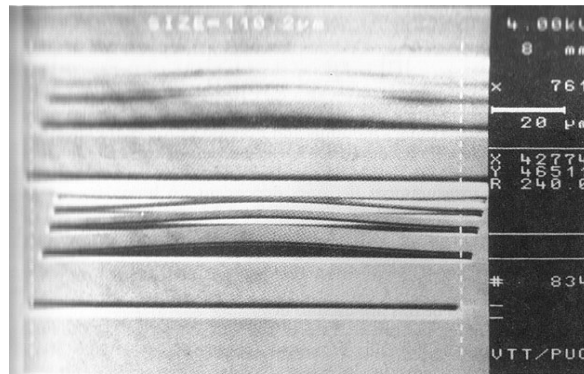
123

Residual Stress Measurement



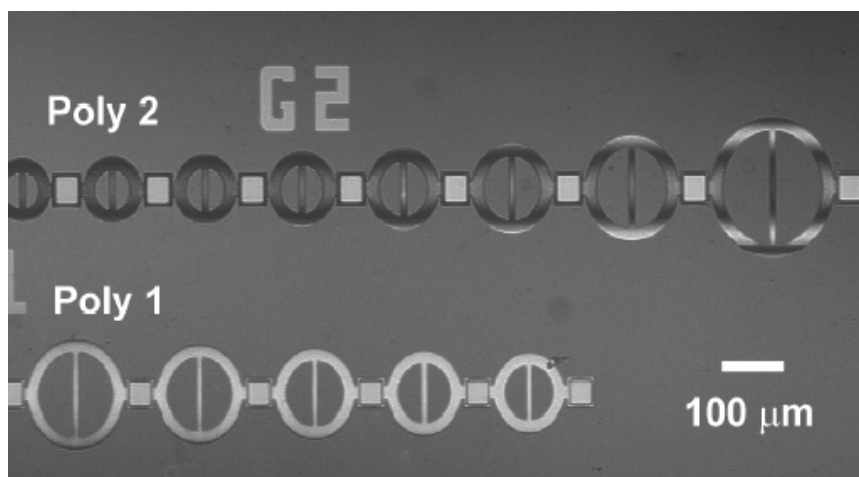


Buckling



127

薄膜製程殘留應力量測

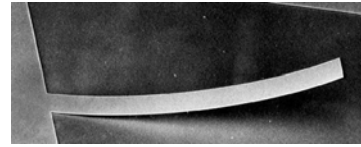


Guckel's Rings: for tensile residual stress evaluation

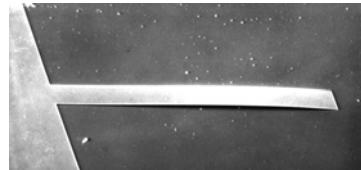
128

Residual Stress Gradient

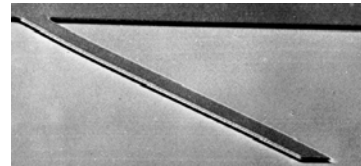
Tensile thin film on top



Compressive thin film on top

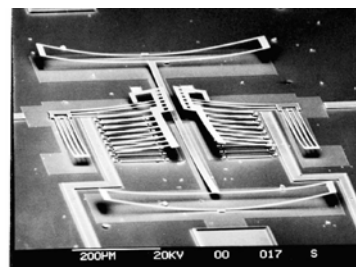
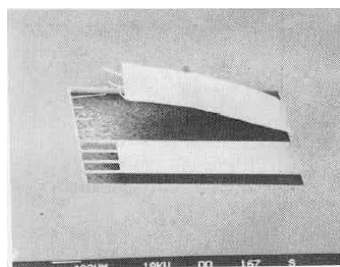


Perfect! Annealing poly-Silicon within oxide layers with similar phosphorous content at 1000°C for 60 seconds or so is enough!



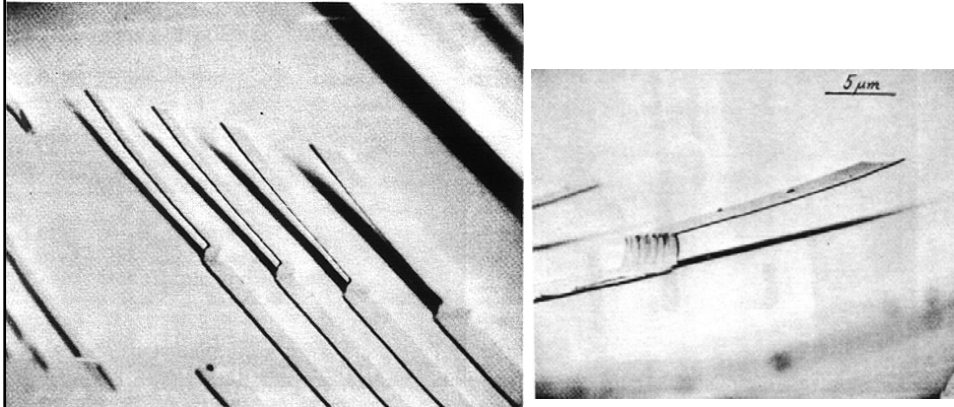
129

Failure due to Residual Stress Gradient



130

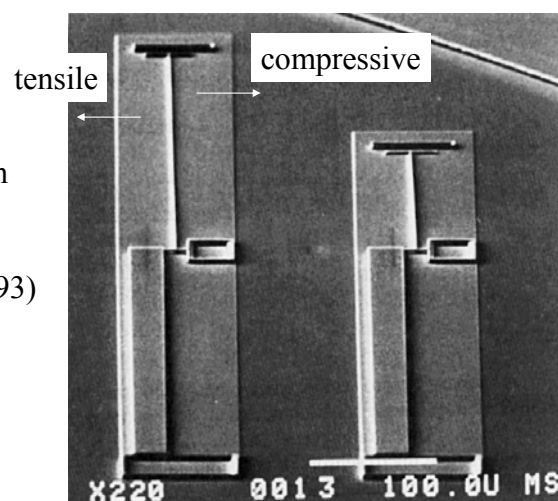
Stress Gradient



131

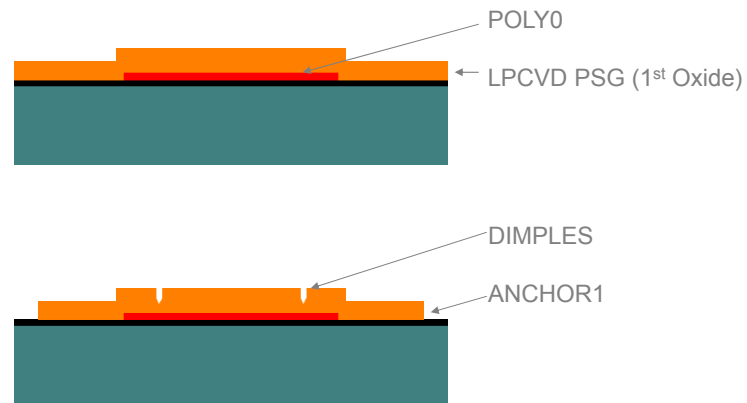
薄膜製程殘留應力量測

- In situ measurement for residual stress during thin film processing
- (L. Lin, UC Berkeley, 1993)



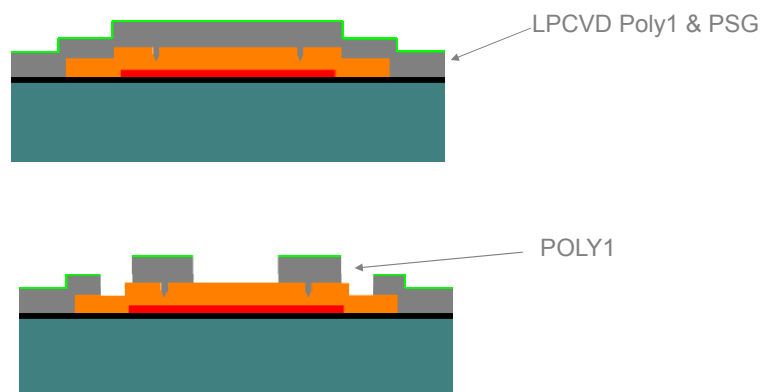
132

Process Overview



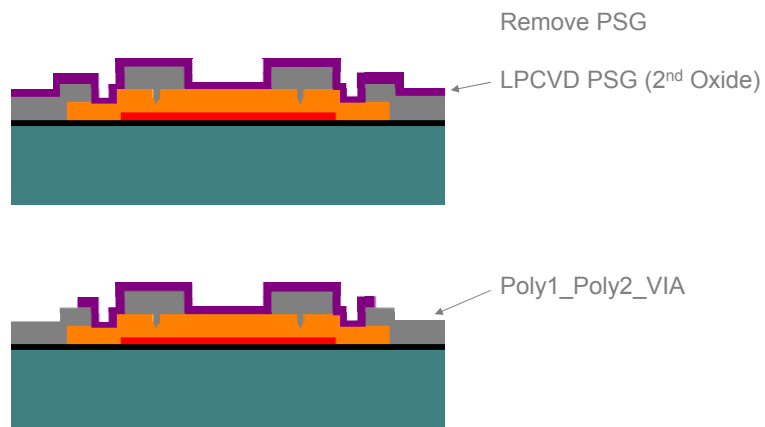
133

Process Overview



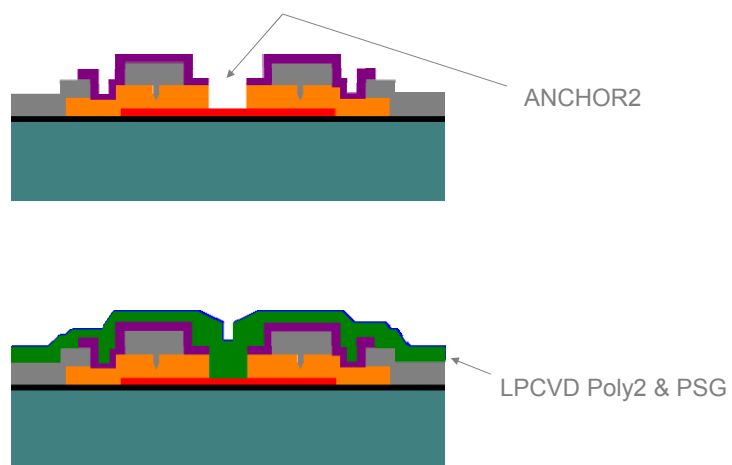
134

Process Overview



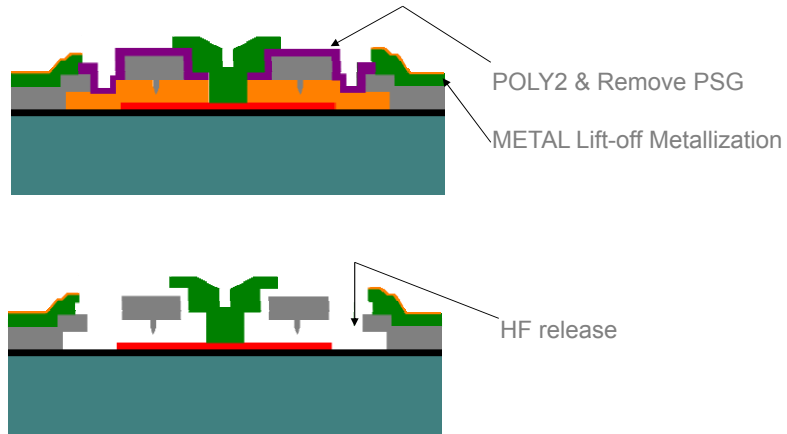
135

Process Overview



136

Process Overview



137